

Fig. 1a

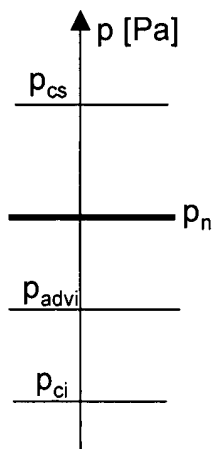


Fig. 1b

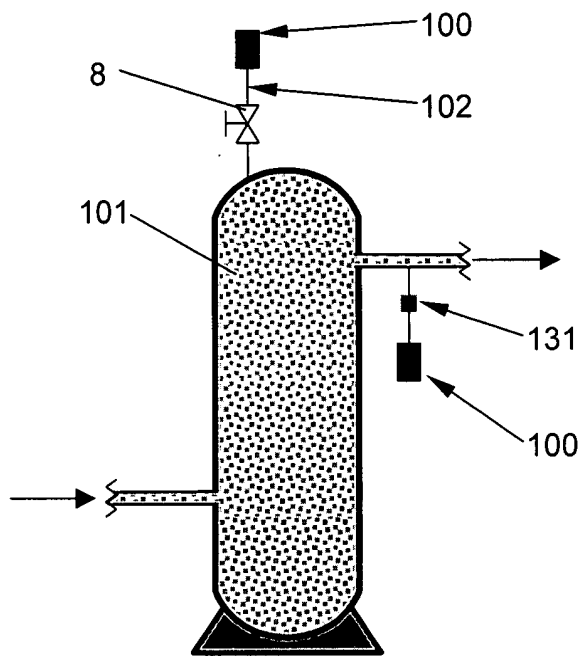


Fig. 2a

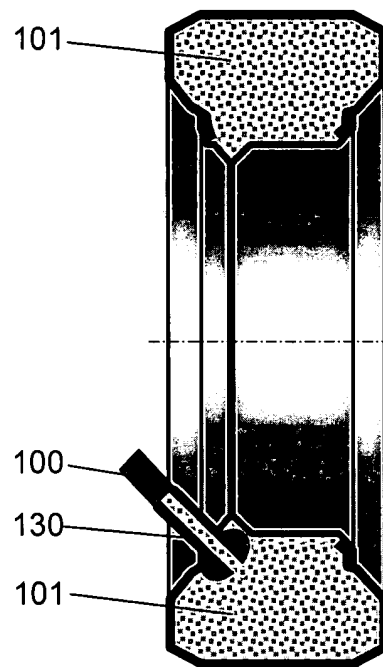


Fig. 2b

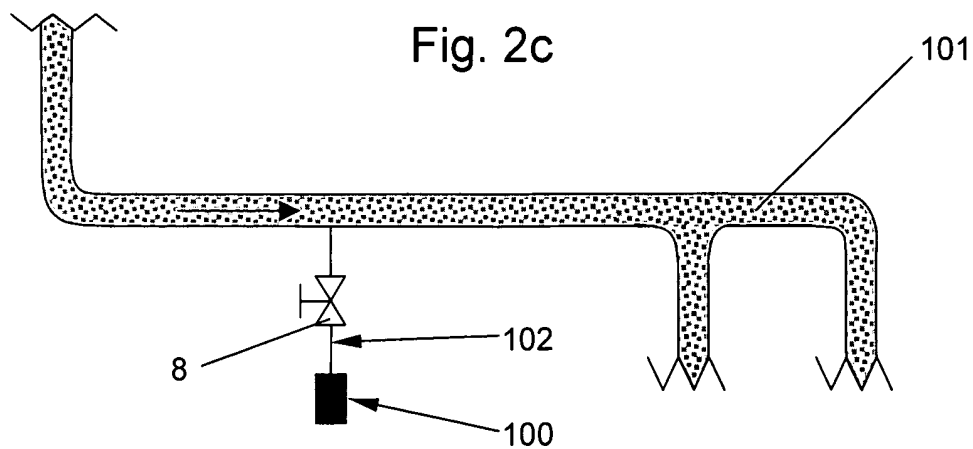
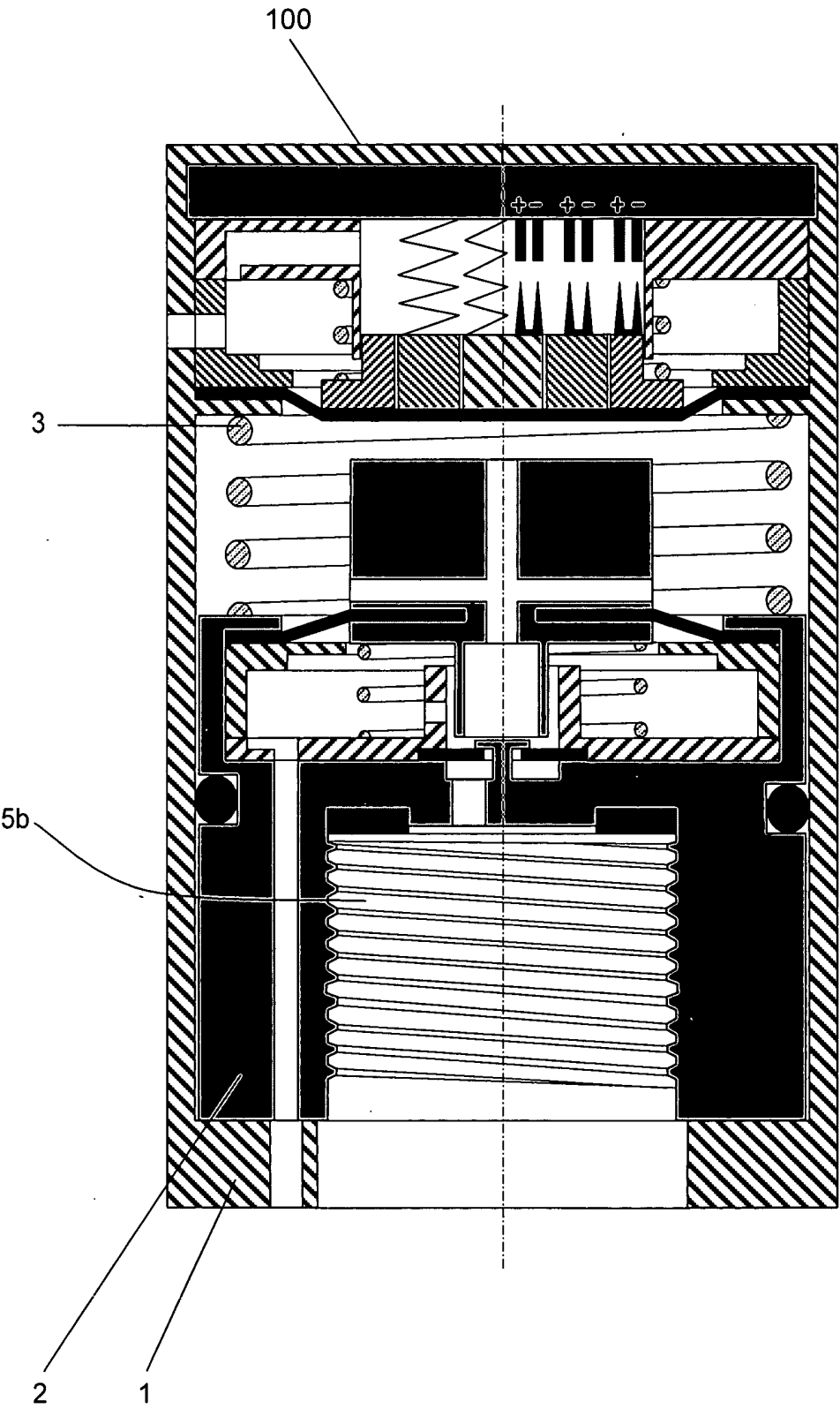


Fig. 2c

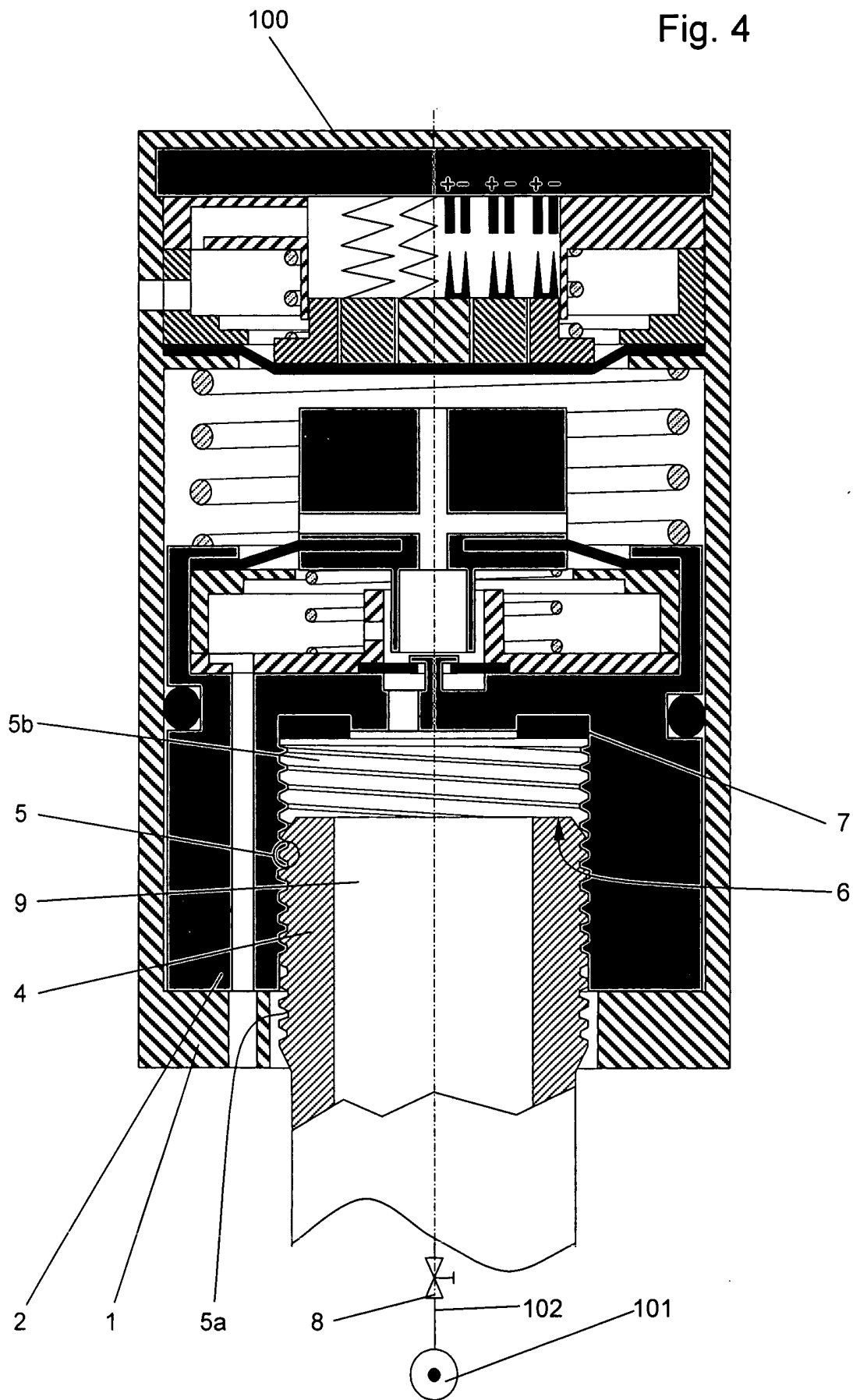
BEST AVAILABLE COPY

Fig. 3



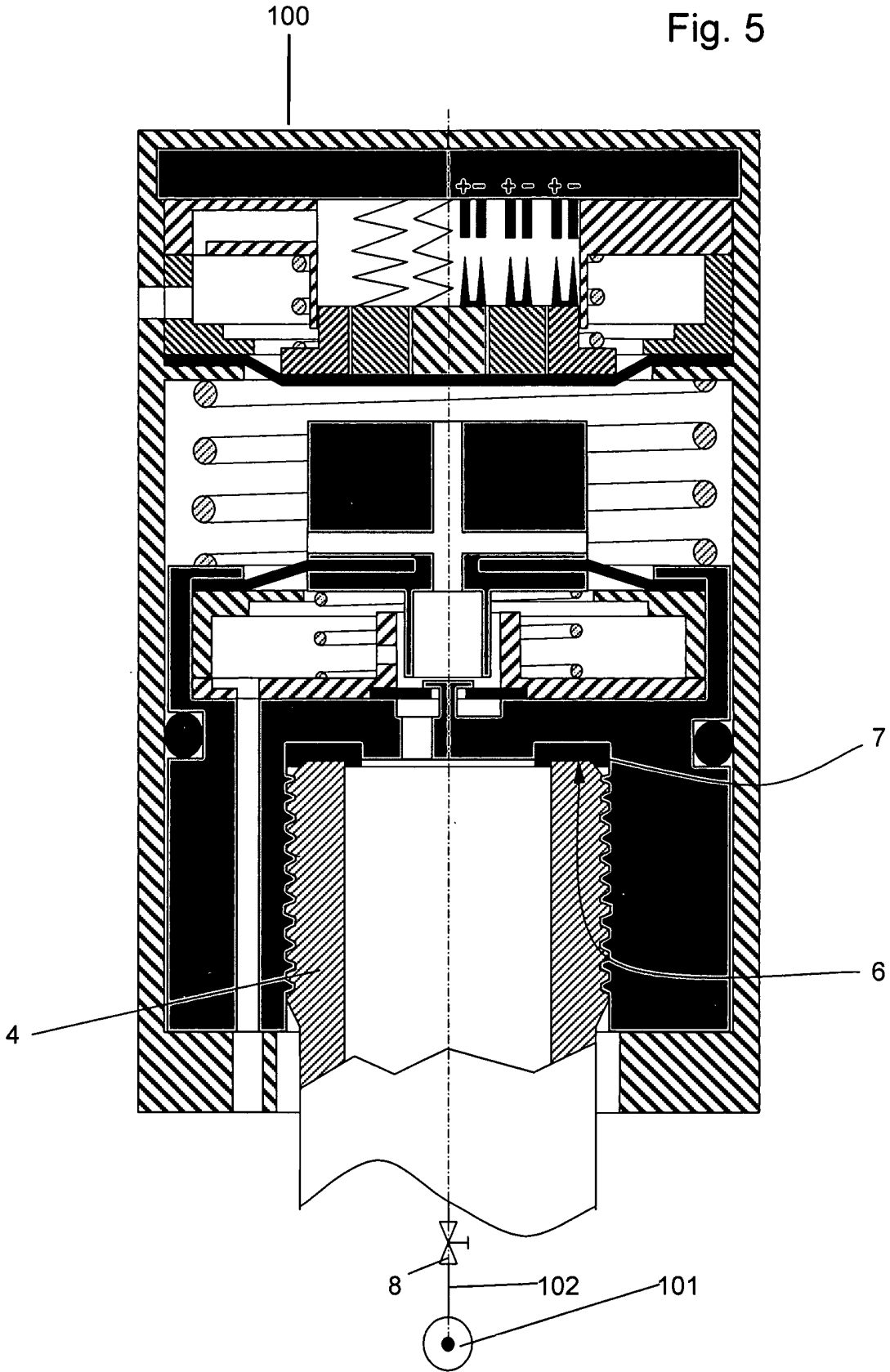
BEST AVAILABLE COPY

Fig. 4



BEST AVAILABLE COPY

Fig. 5



BEST AVAILABLE COPY

Fig. 6a

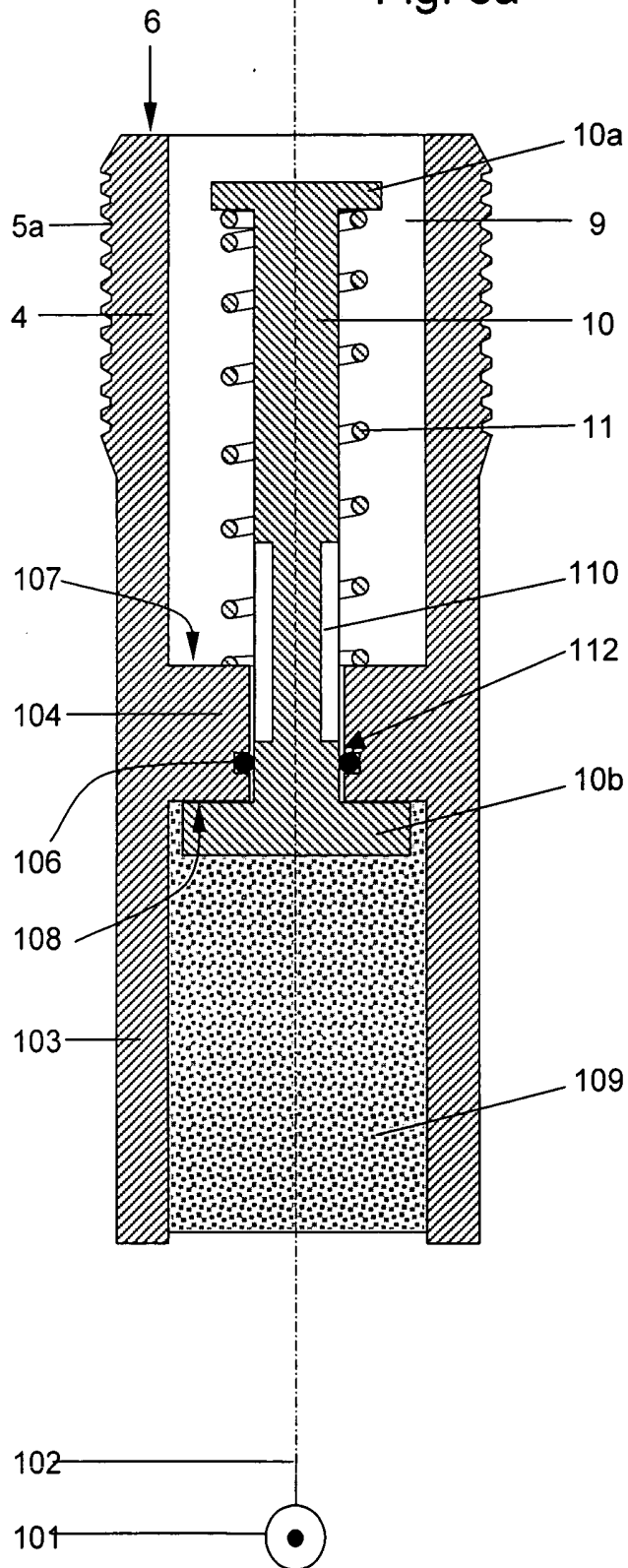


Fig. 6b

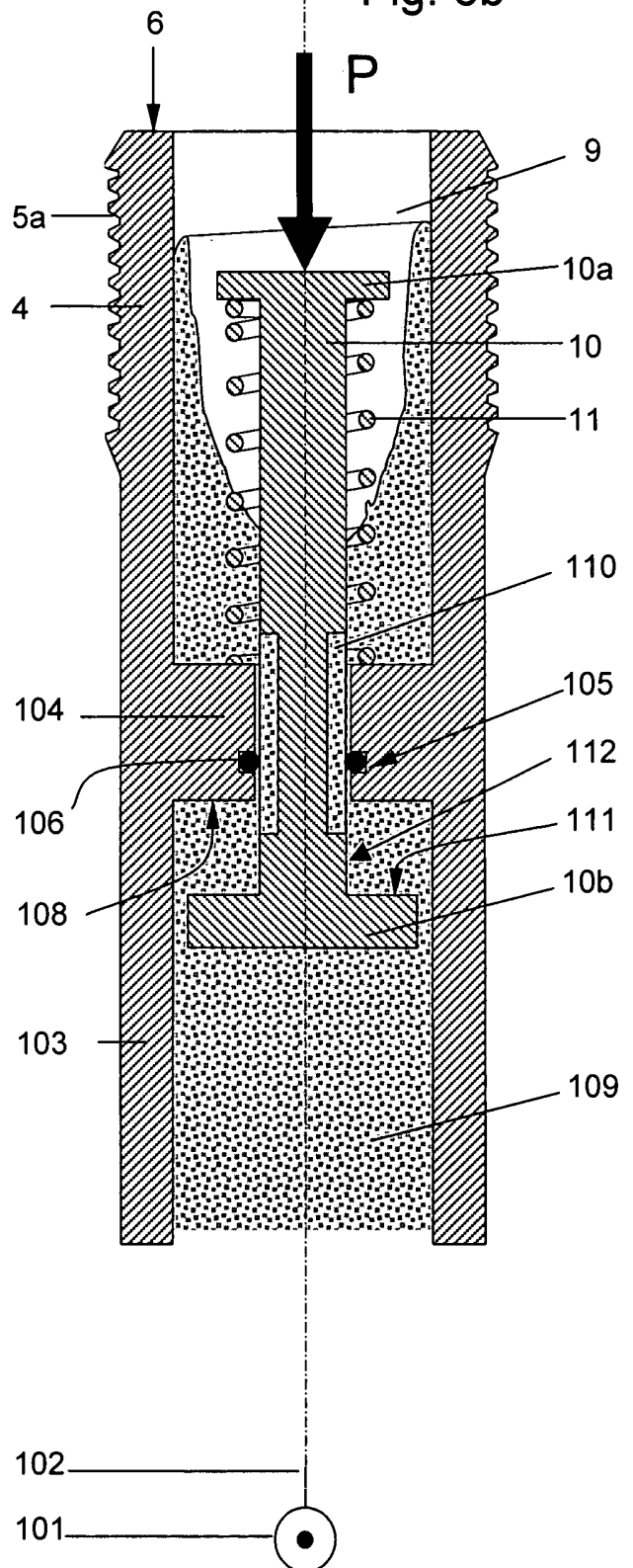


Fig. 7

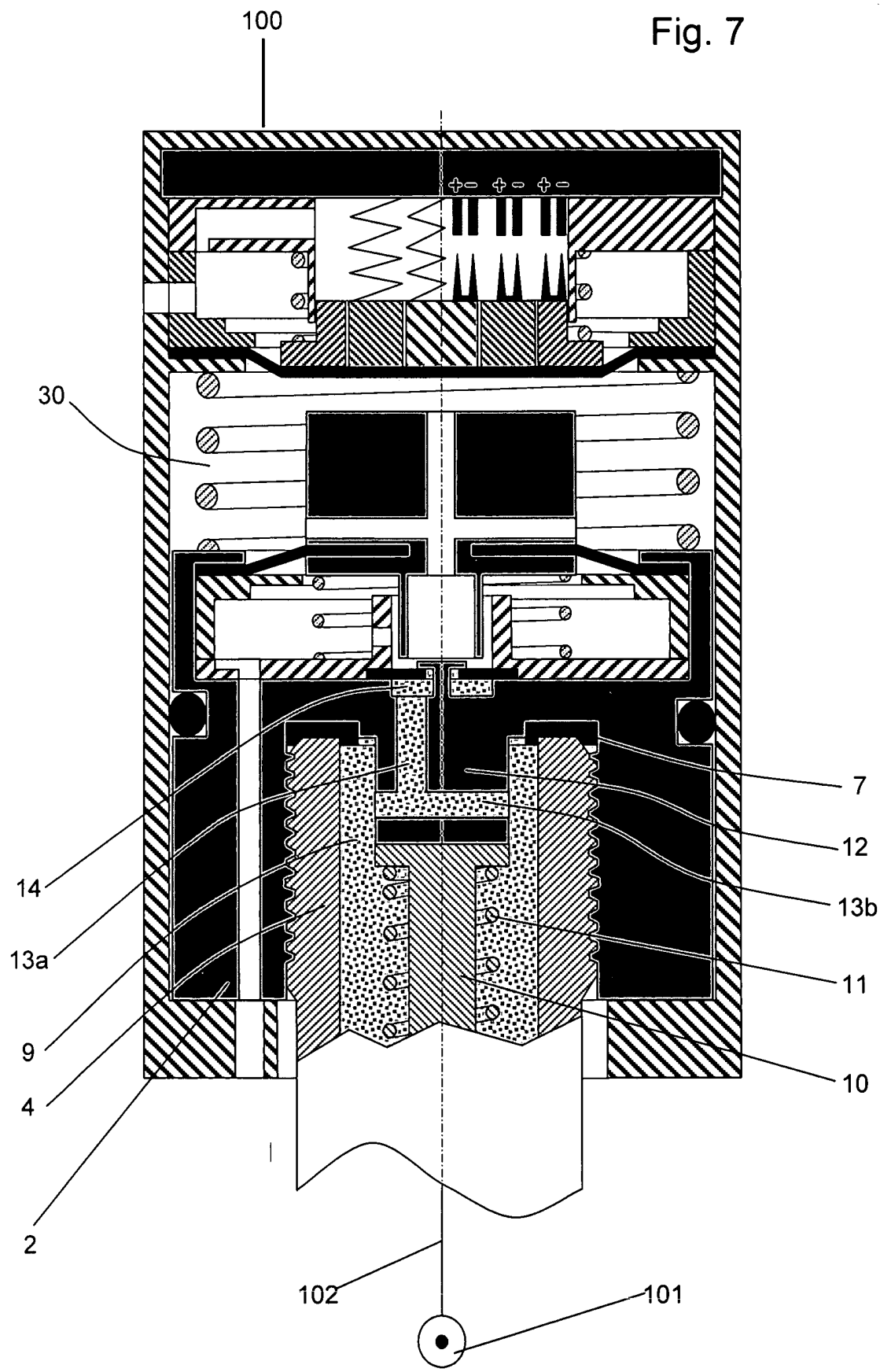


Fig. 8a

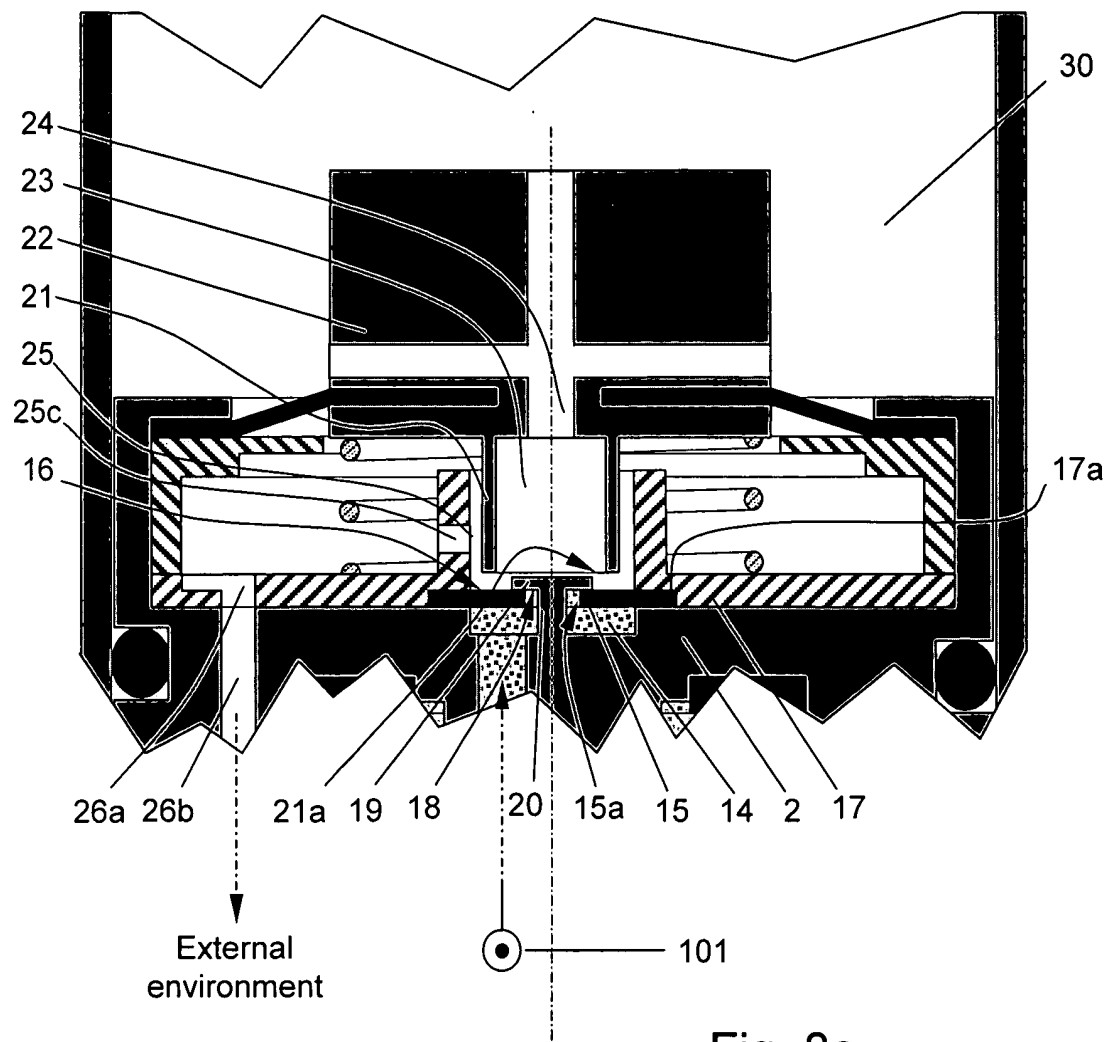


Fig. 8c

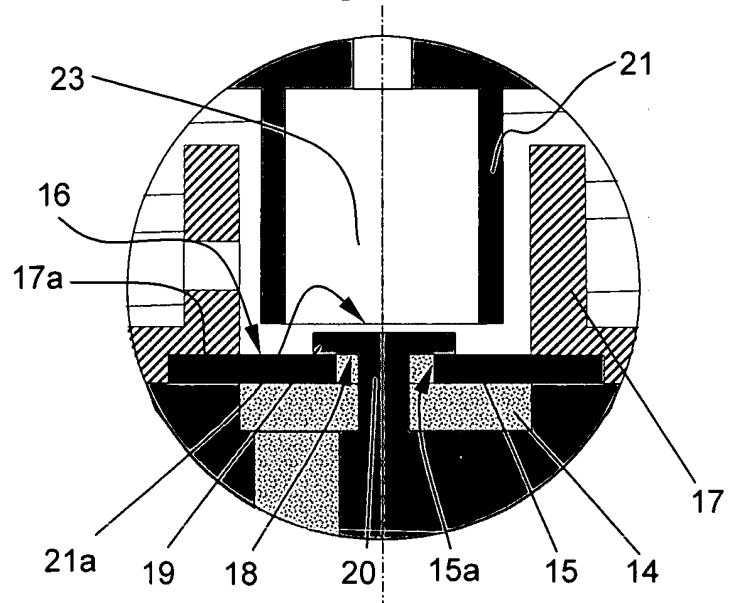


Fig. 8b

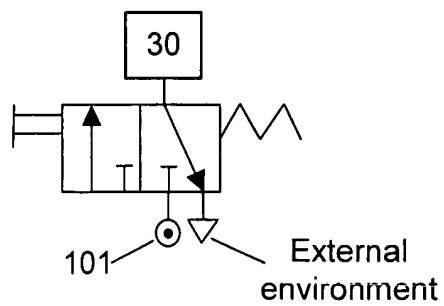




Fig. 9a

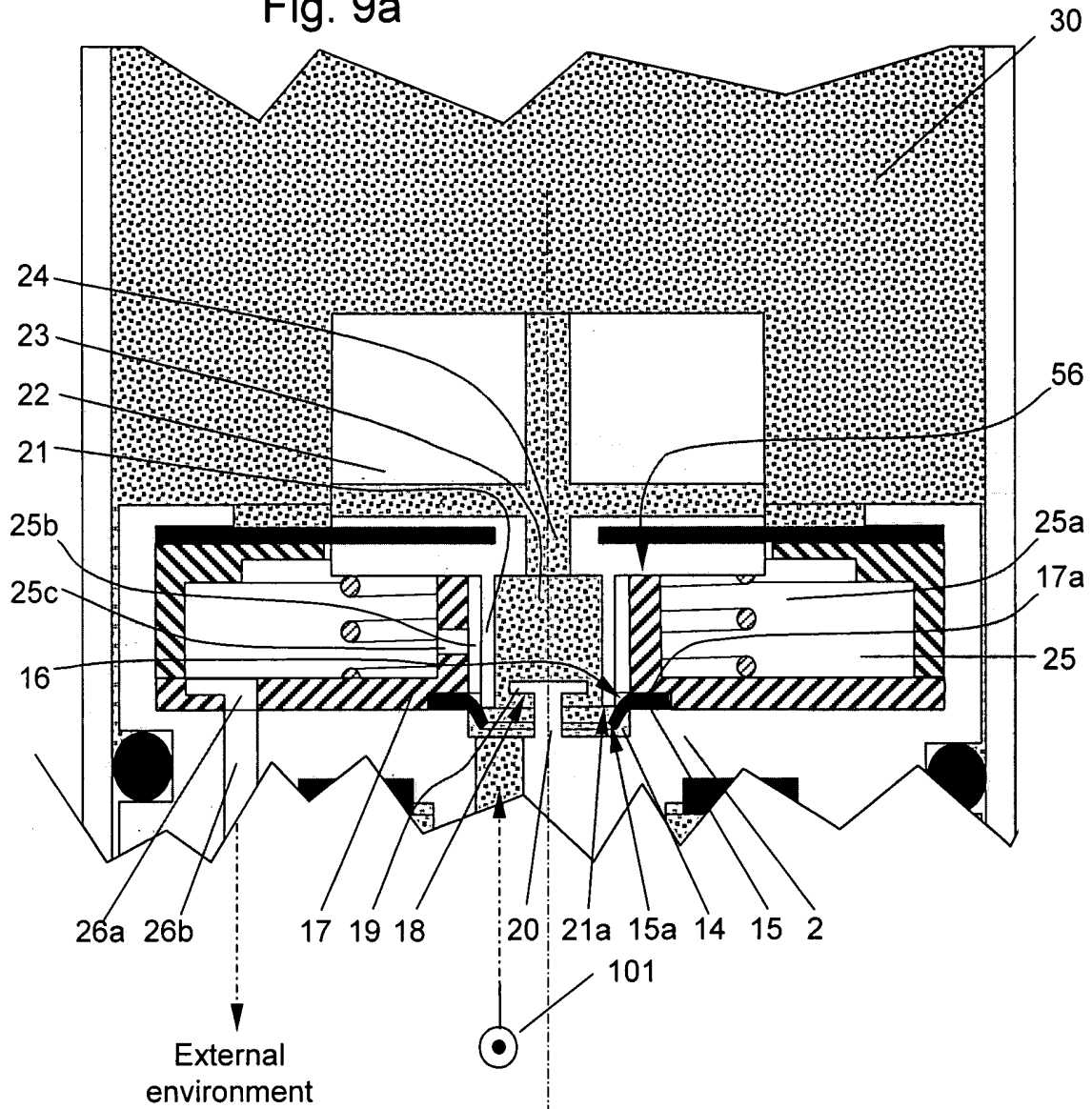


Fig. 9c

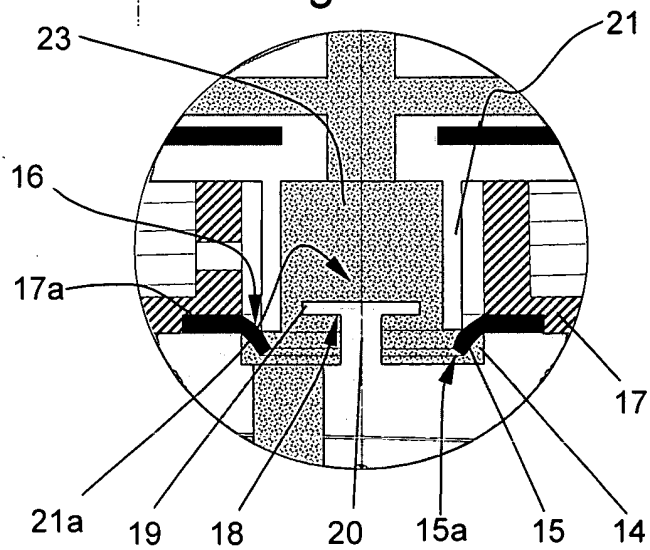
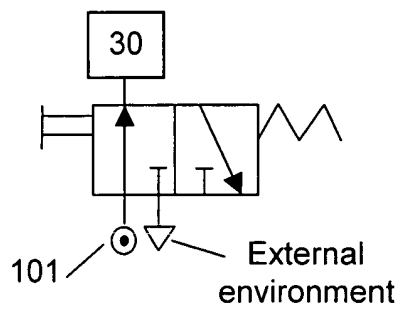


Fig. 9b



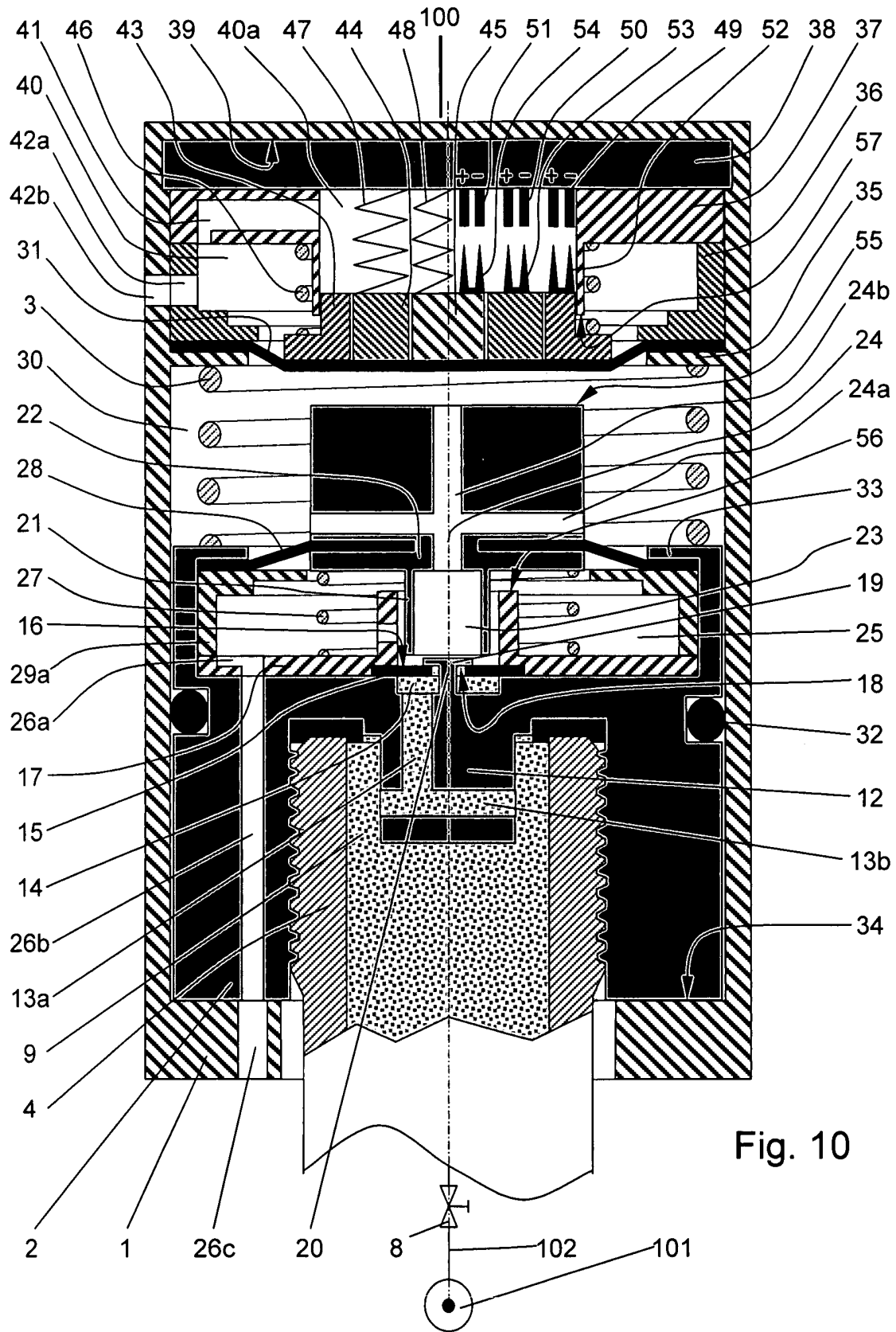
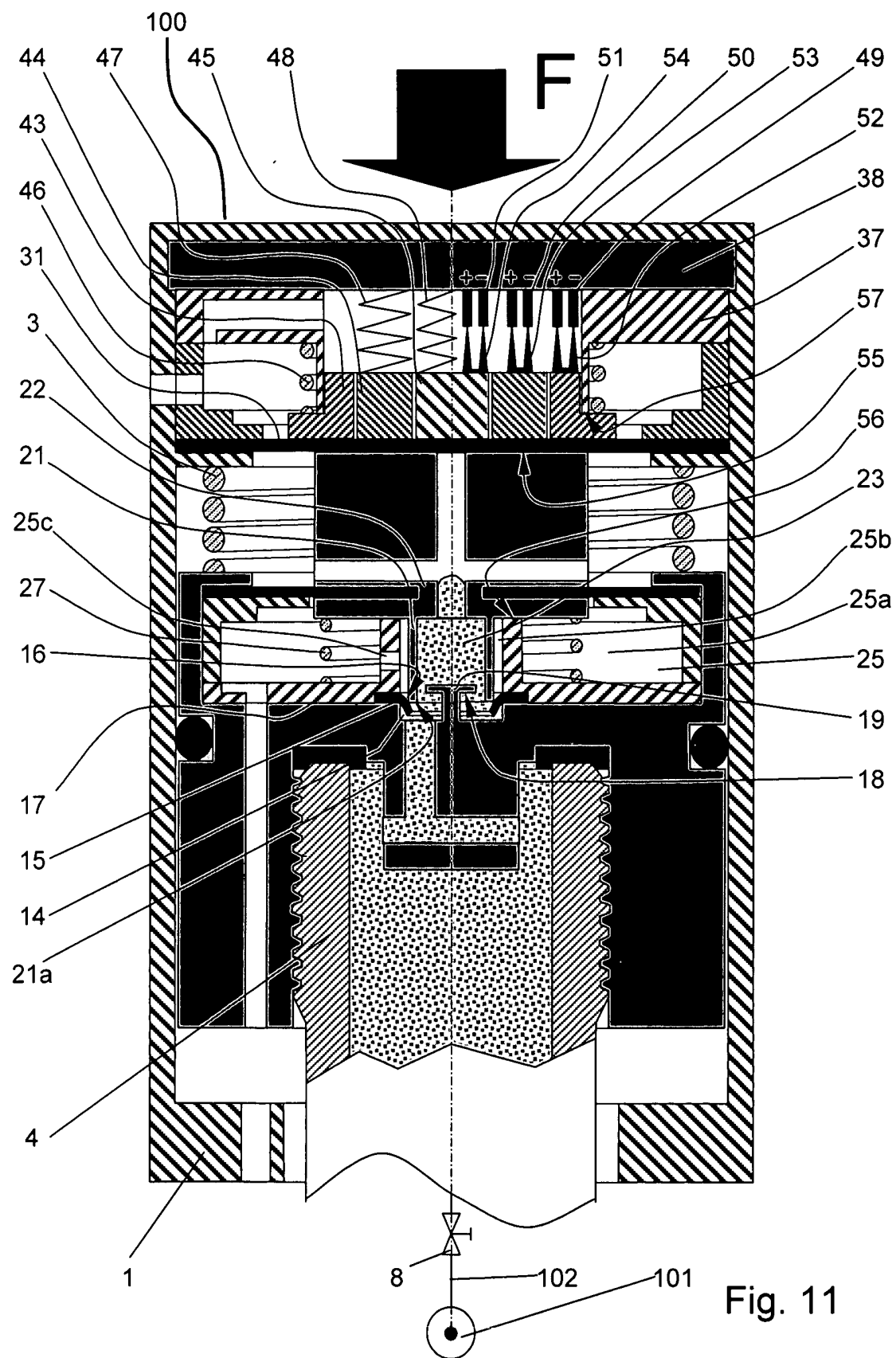


Fig. 10



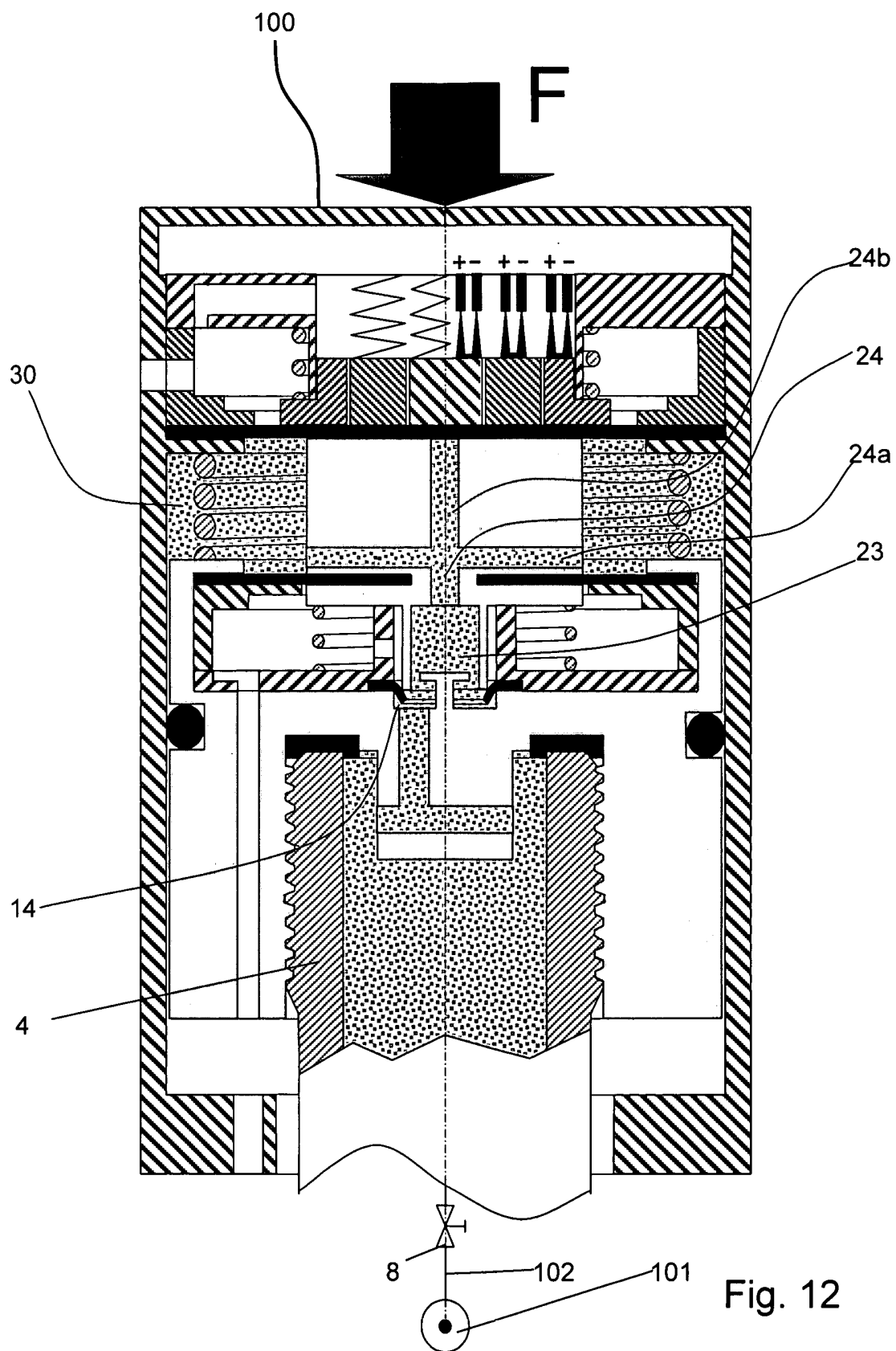
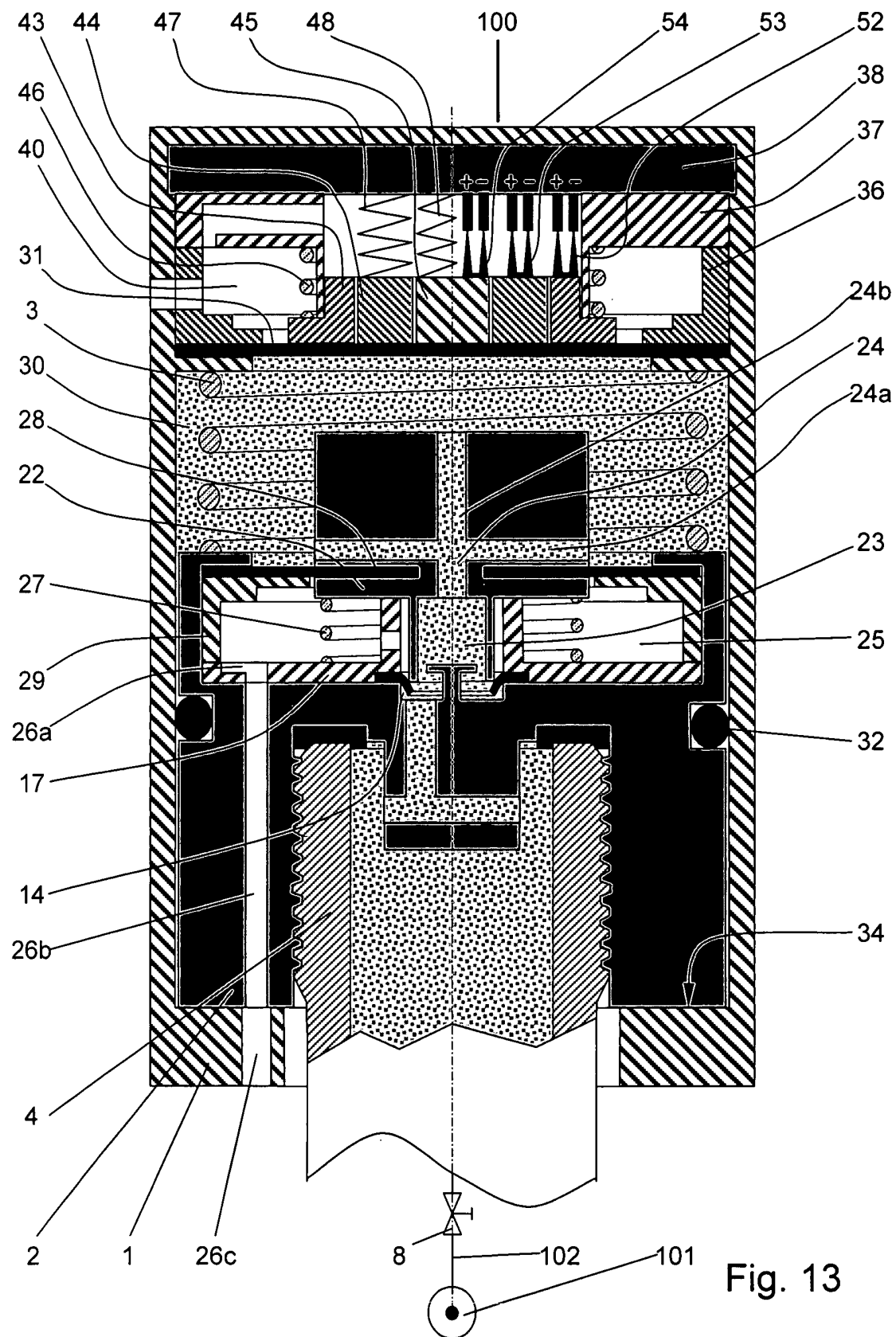


Fig. 12



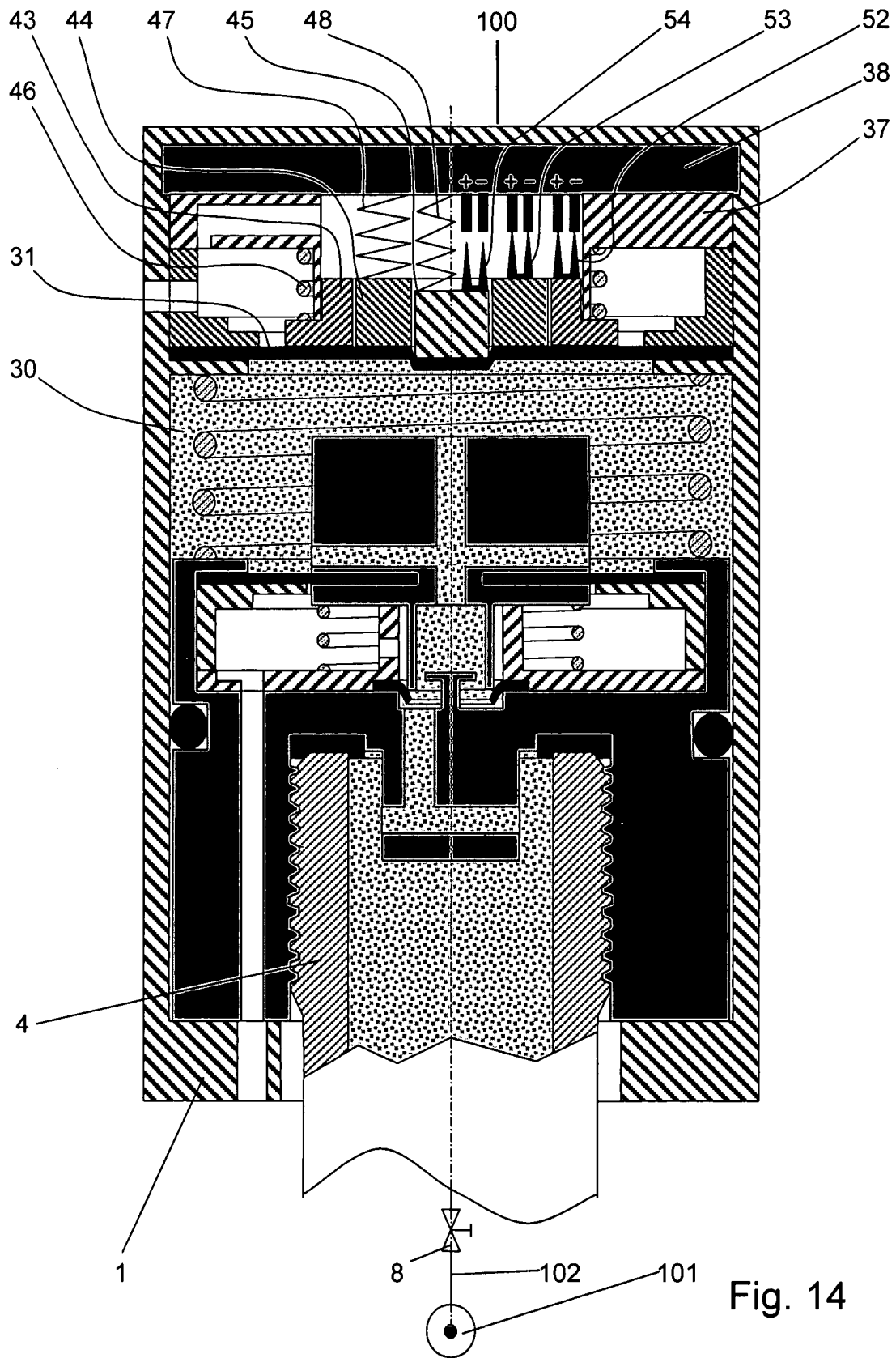


Fig. 14

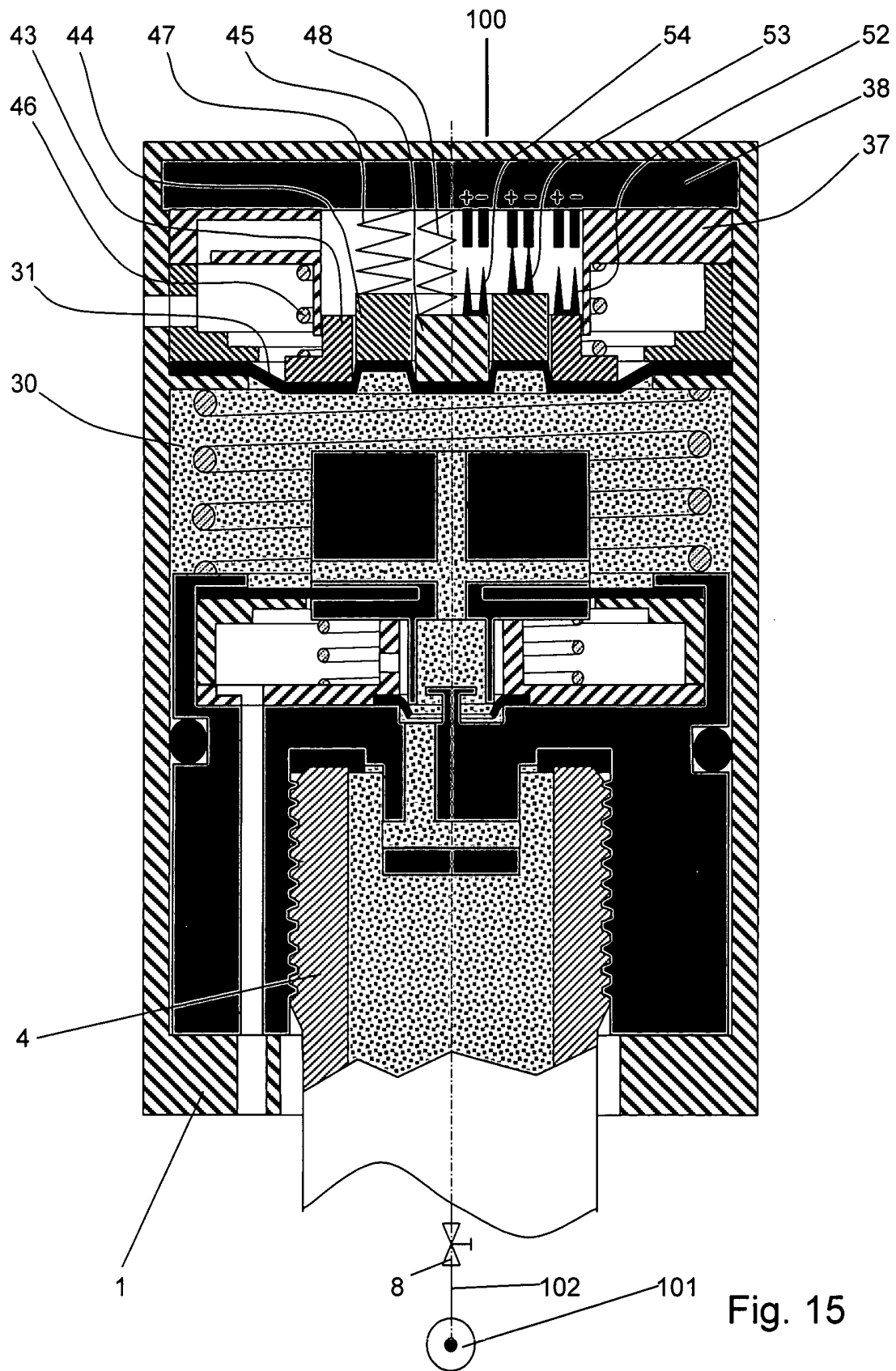


Fig. 15

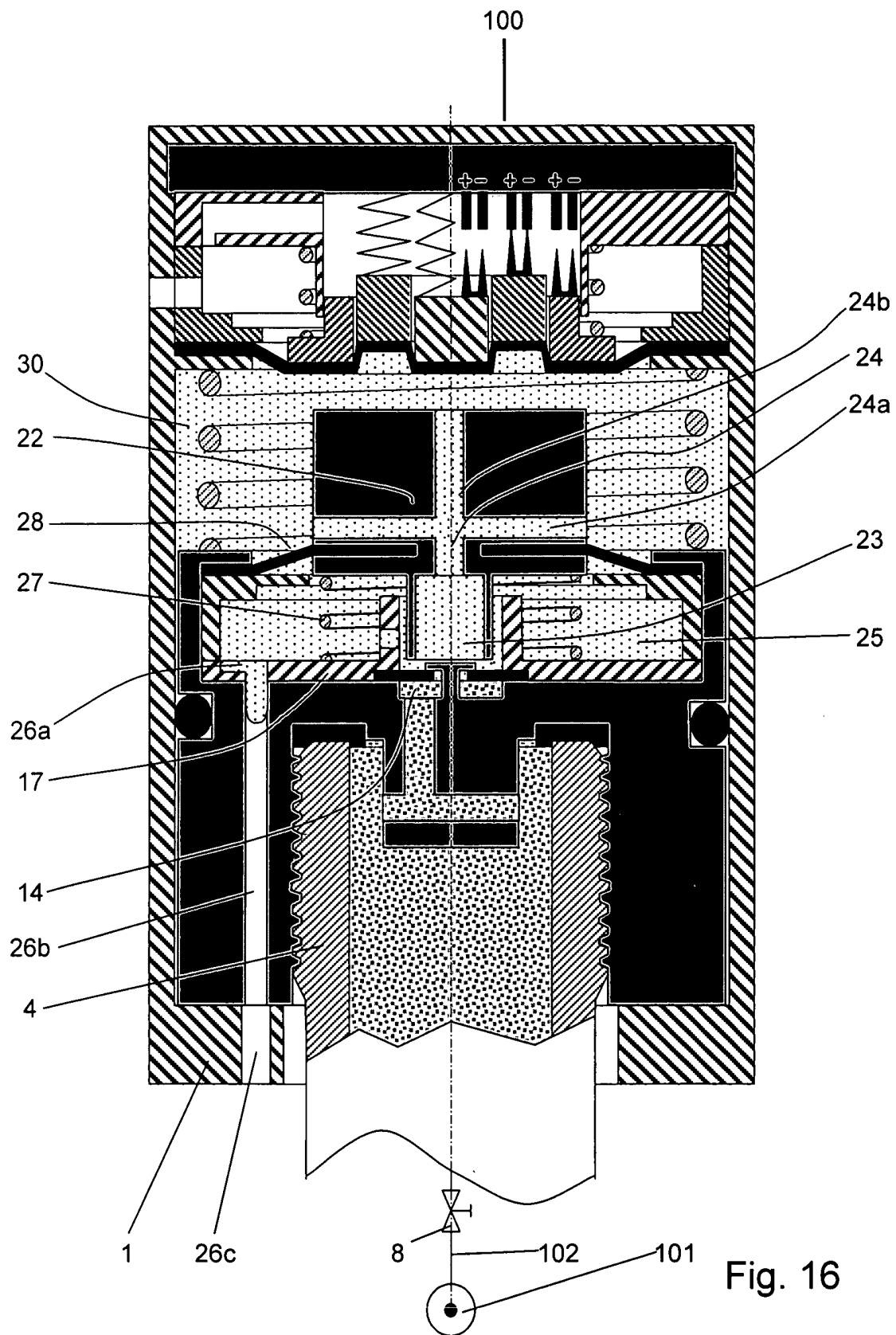


Fig. 16



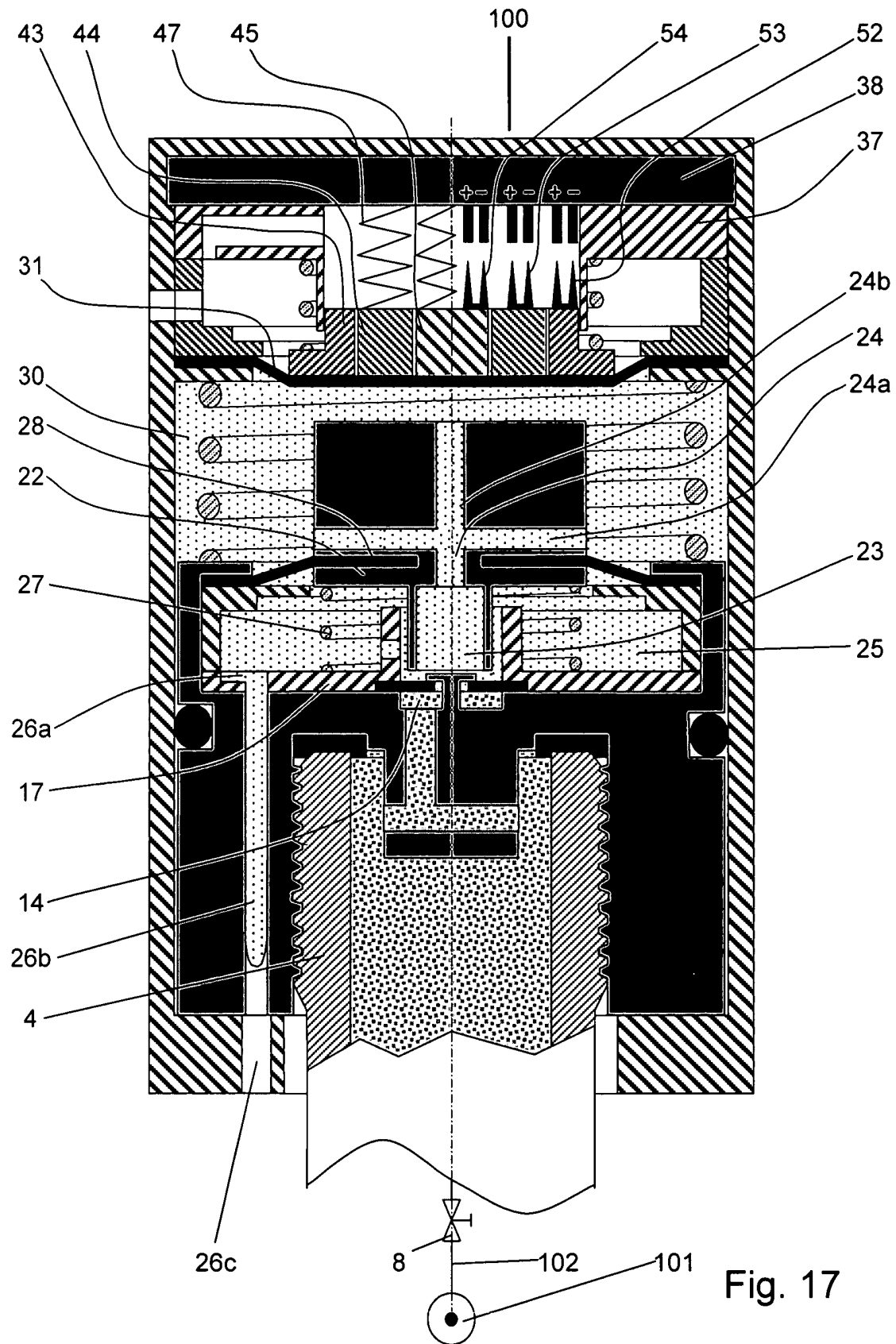


Fig. 17

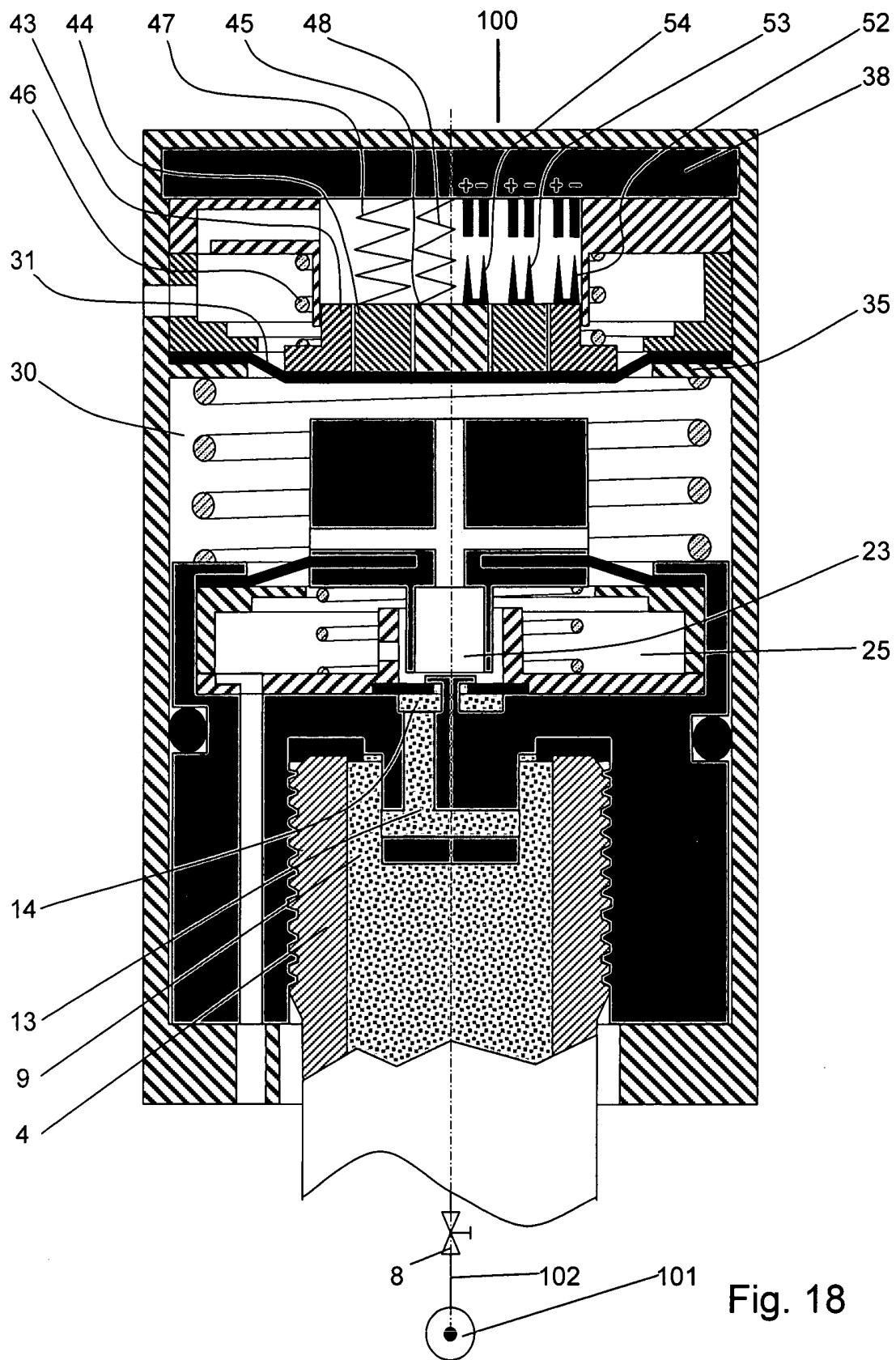


Fig. 18

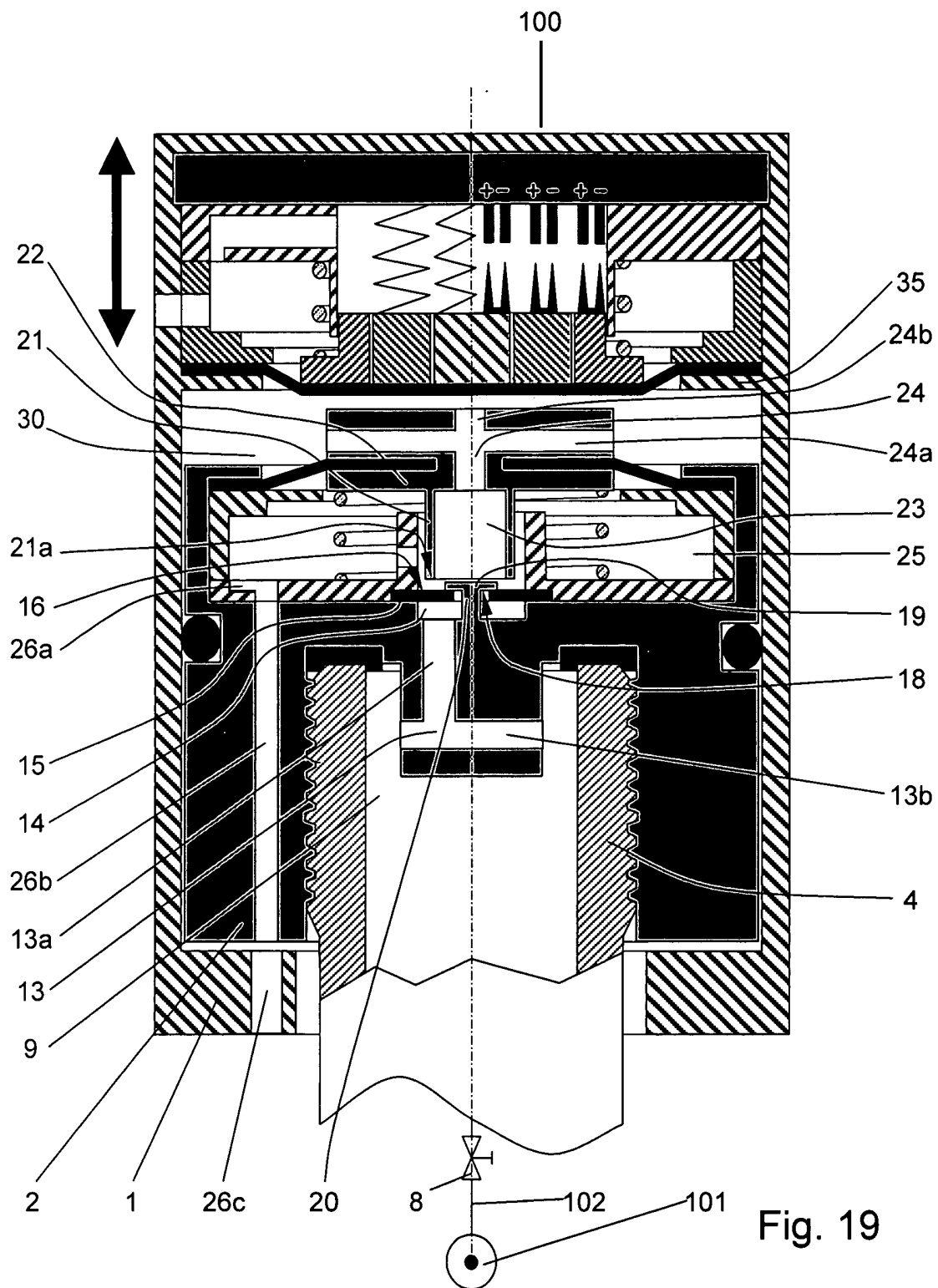


Fig. 19

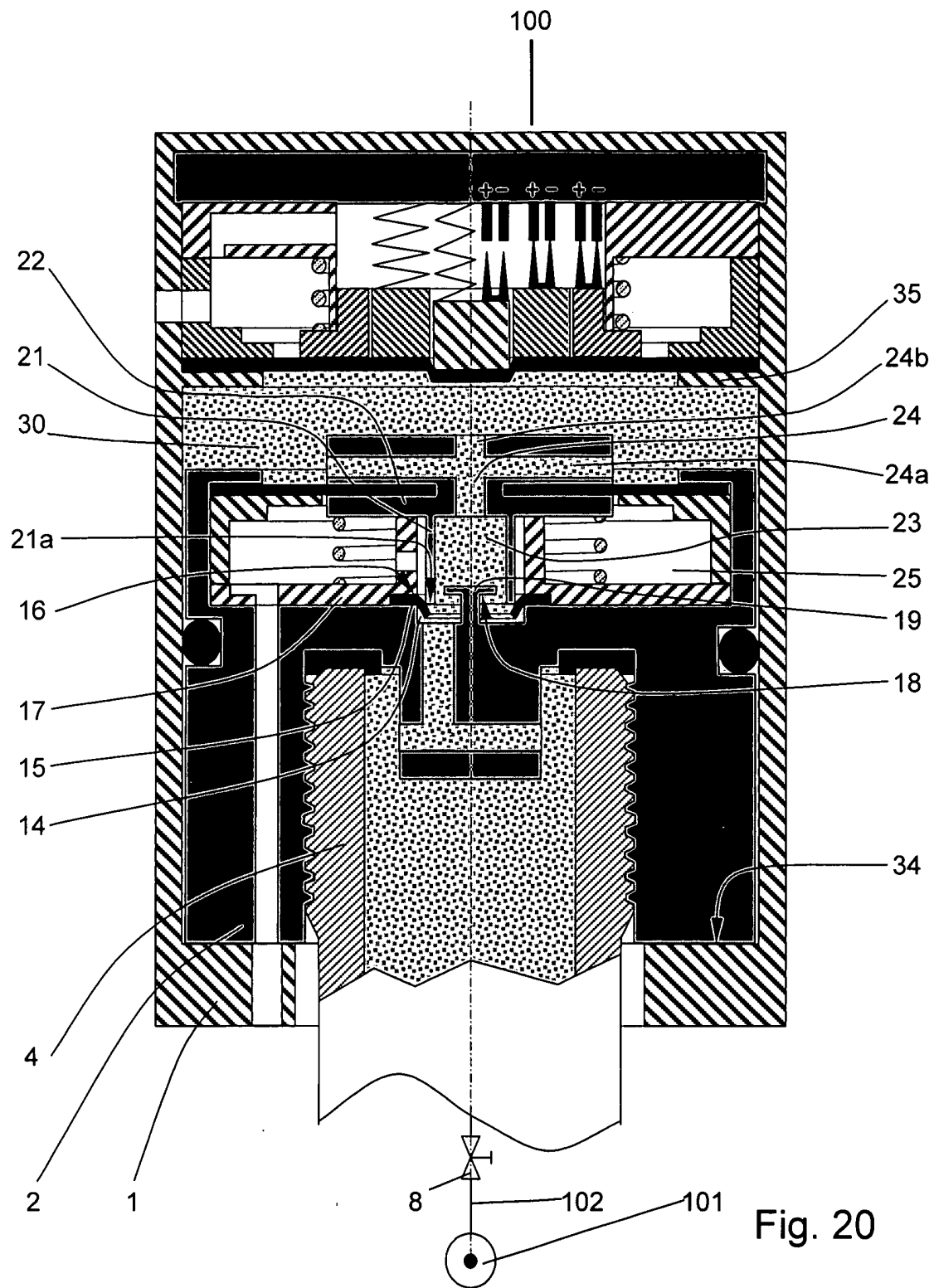


Fig. 20

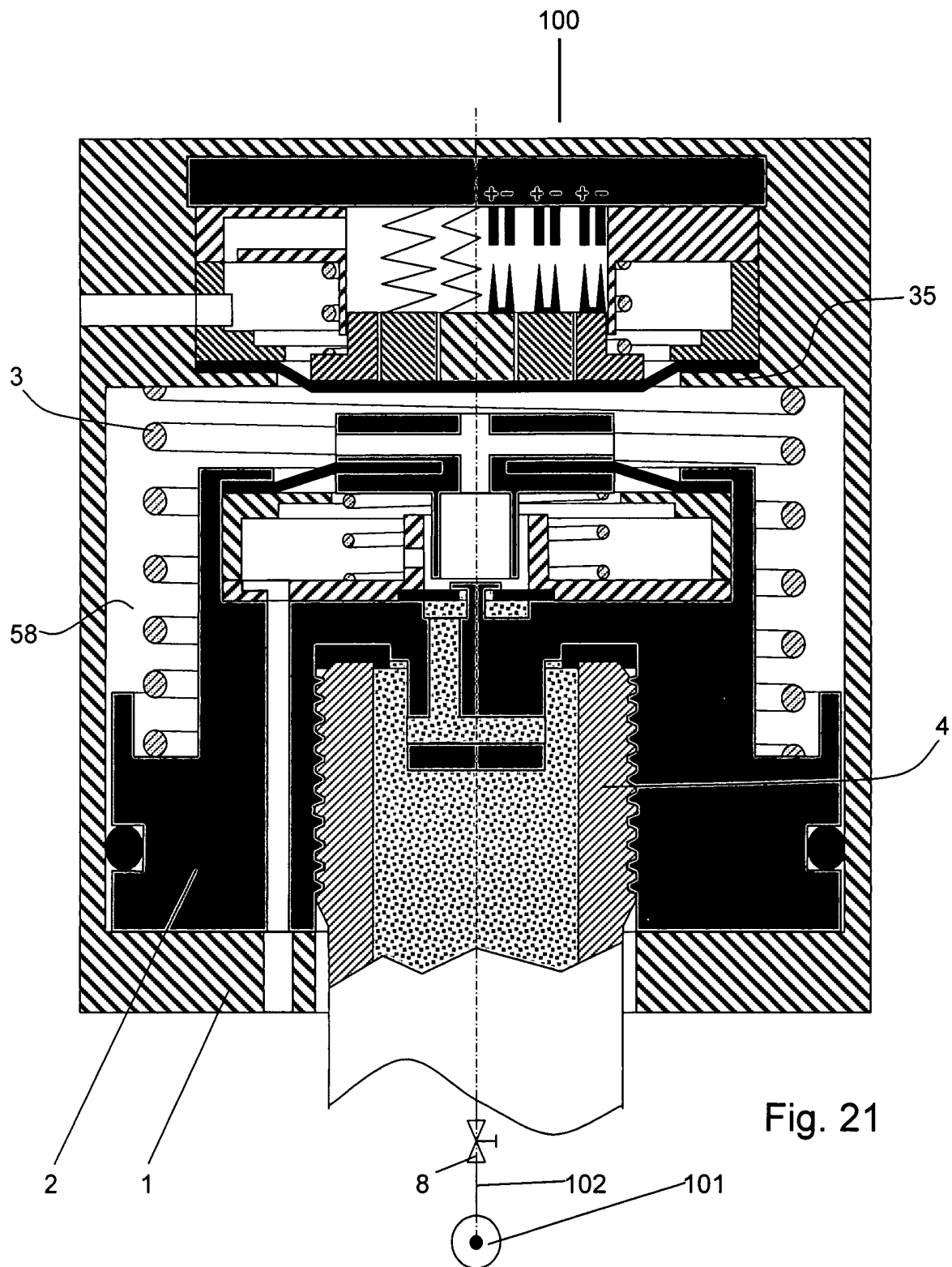


Fig. 21

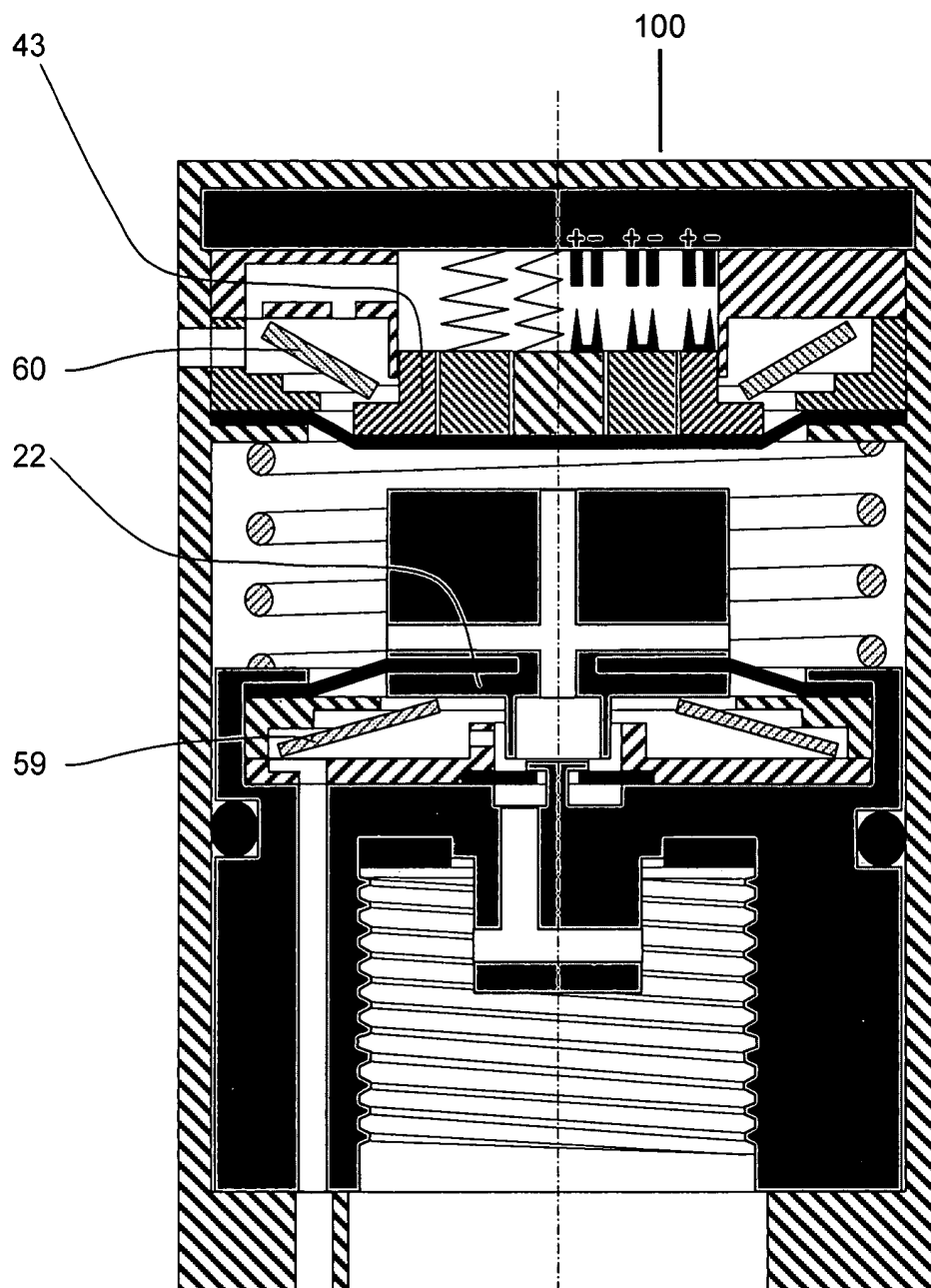
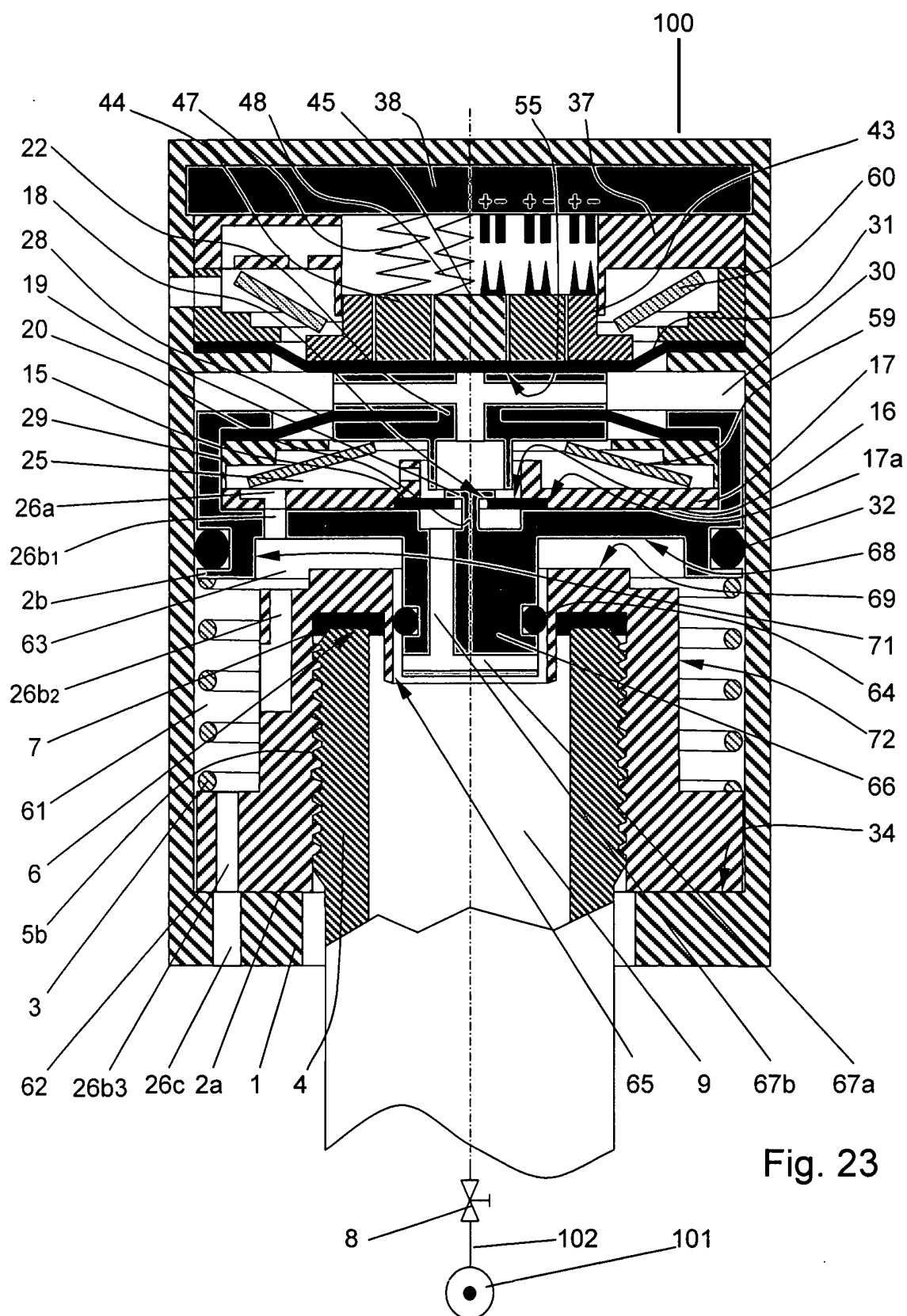
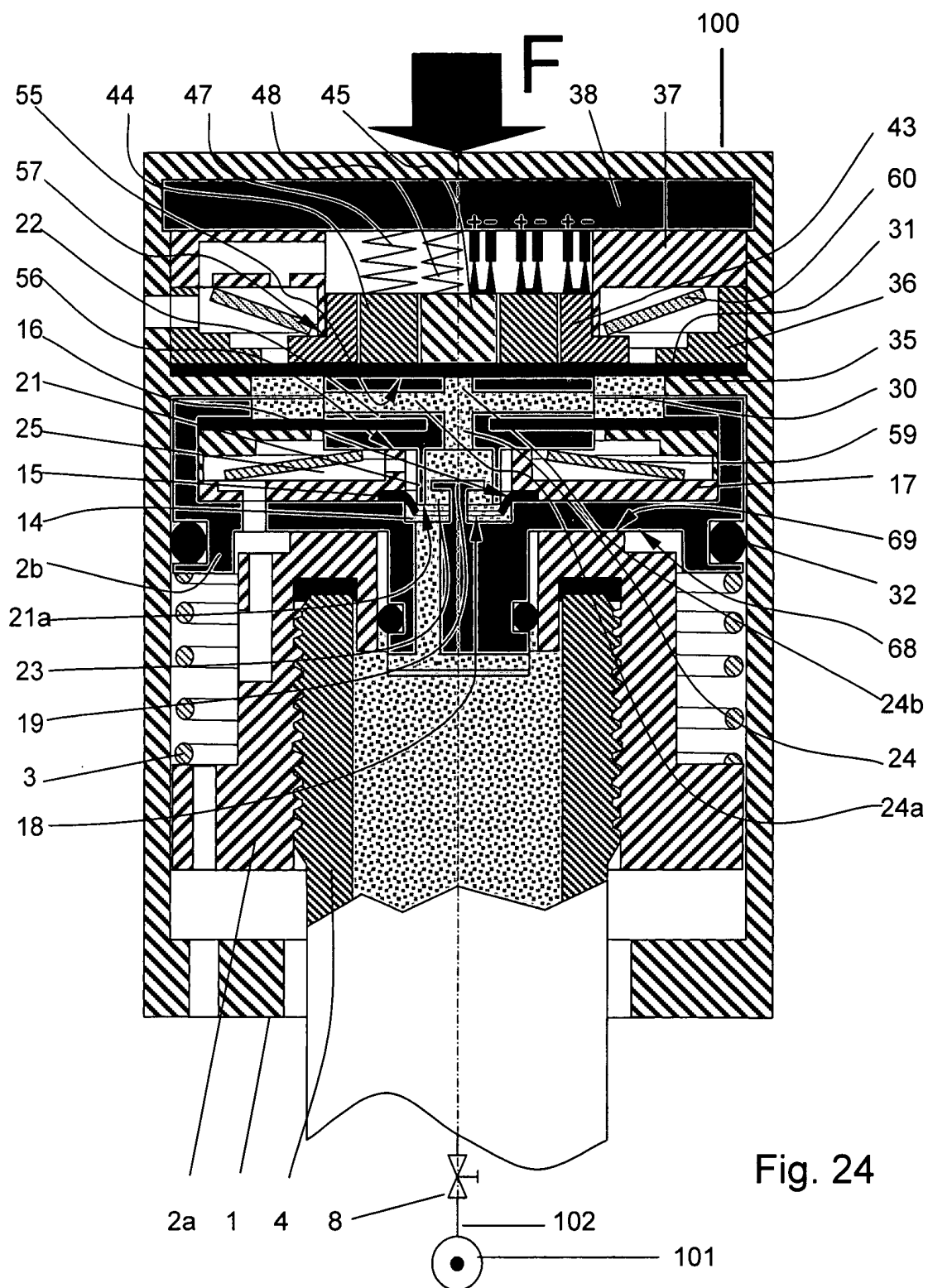


Fig. 22







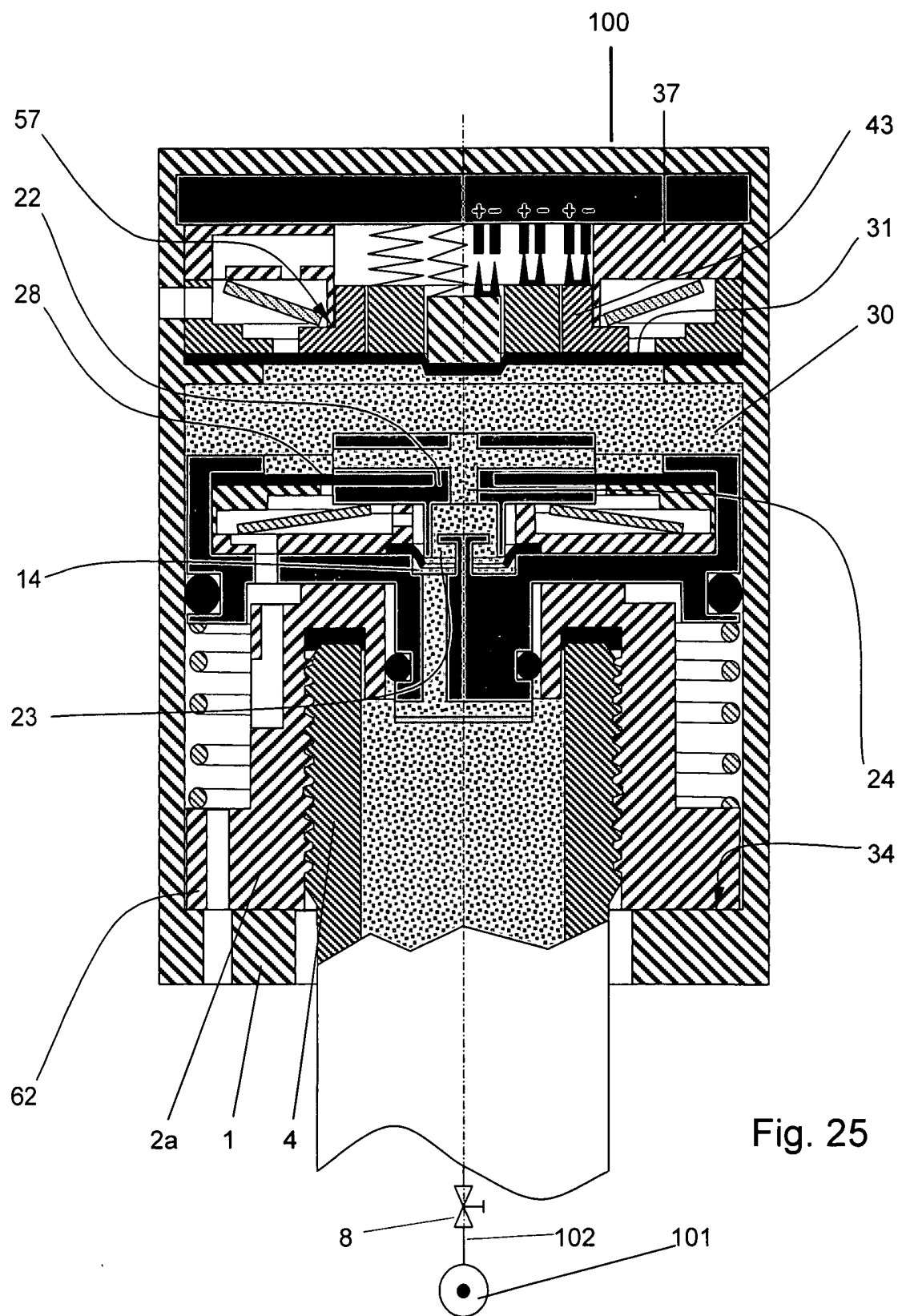
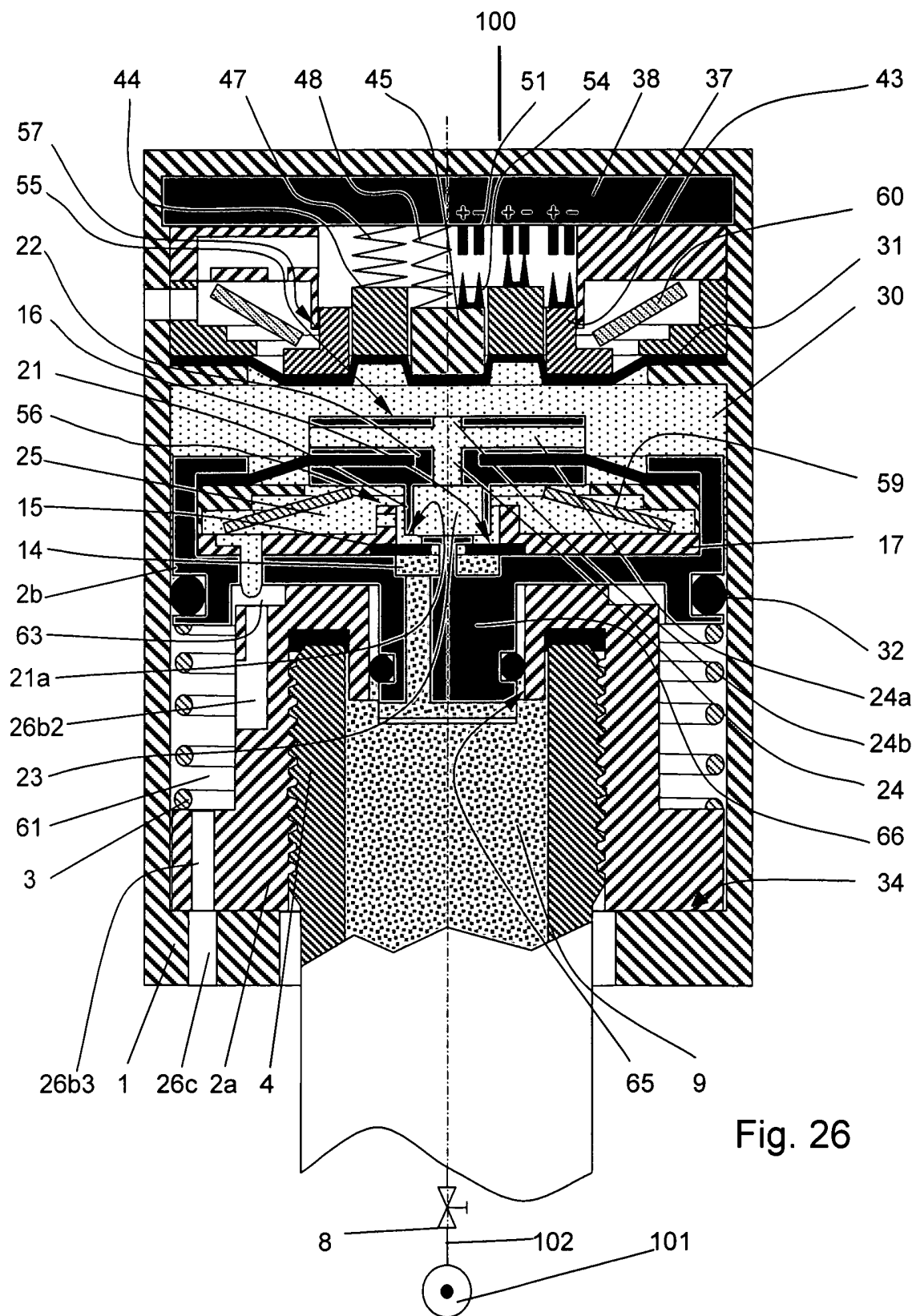
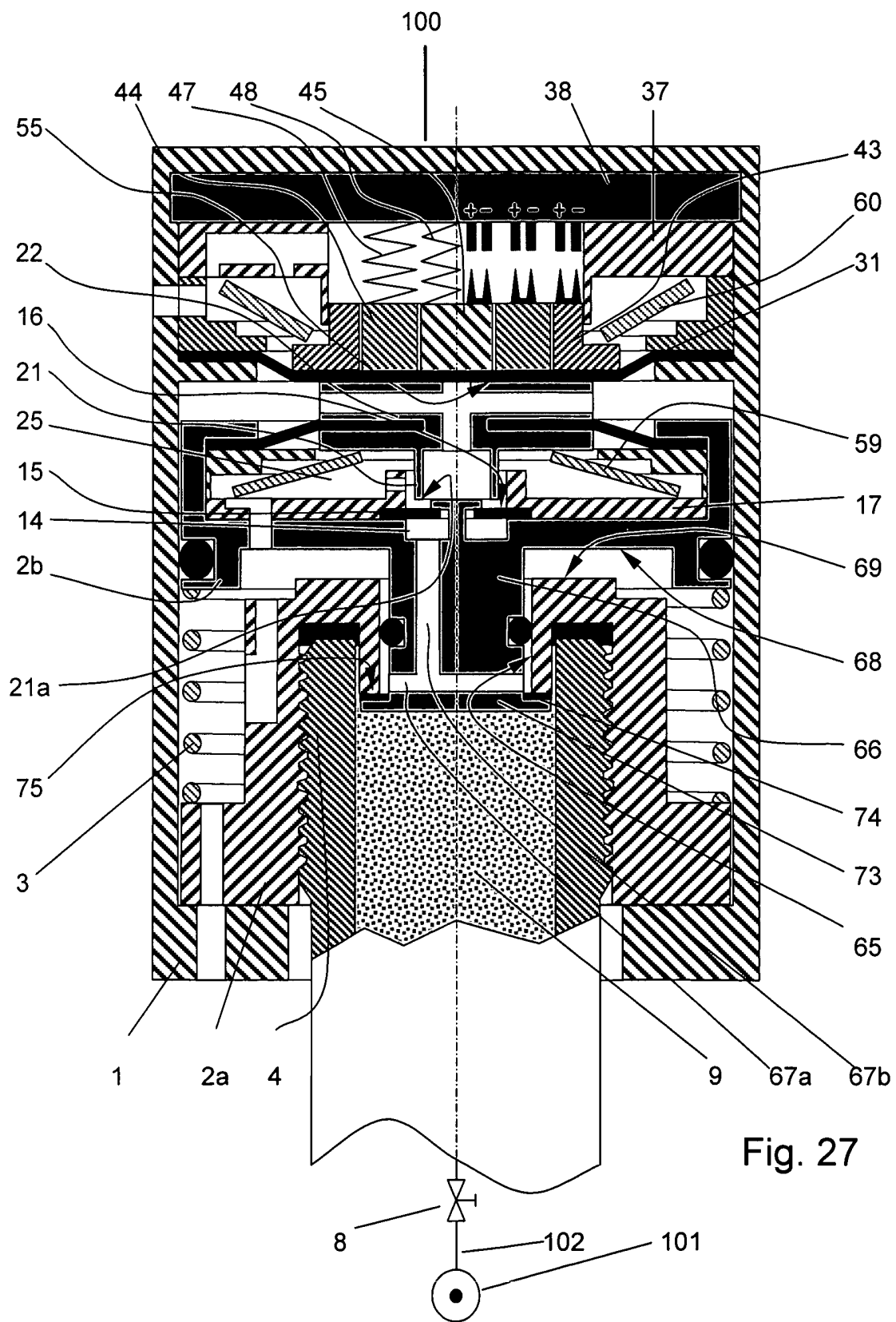
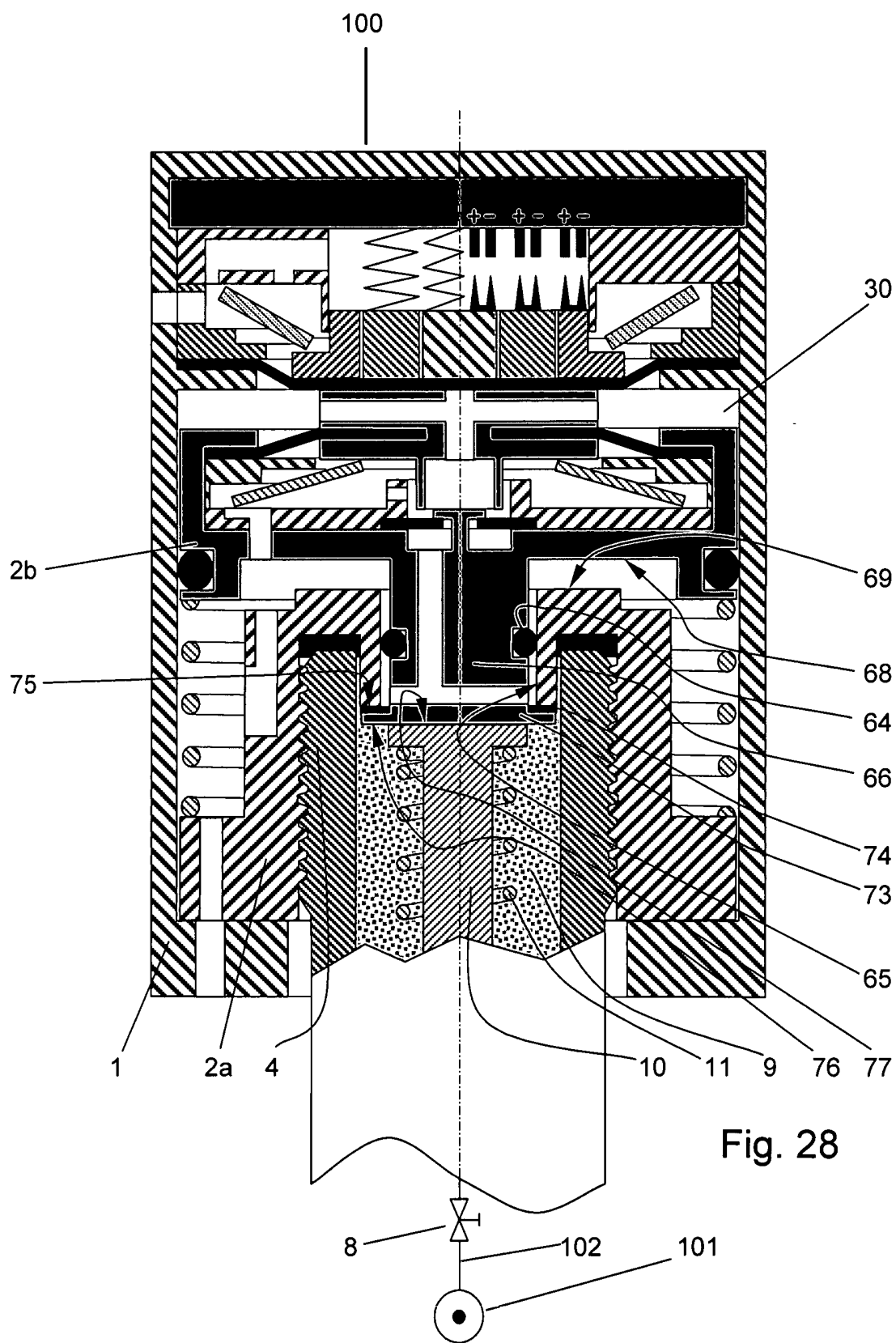


Fig. 25







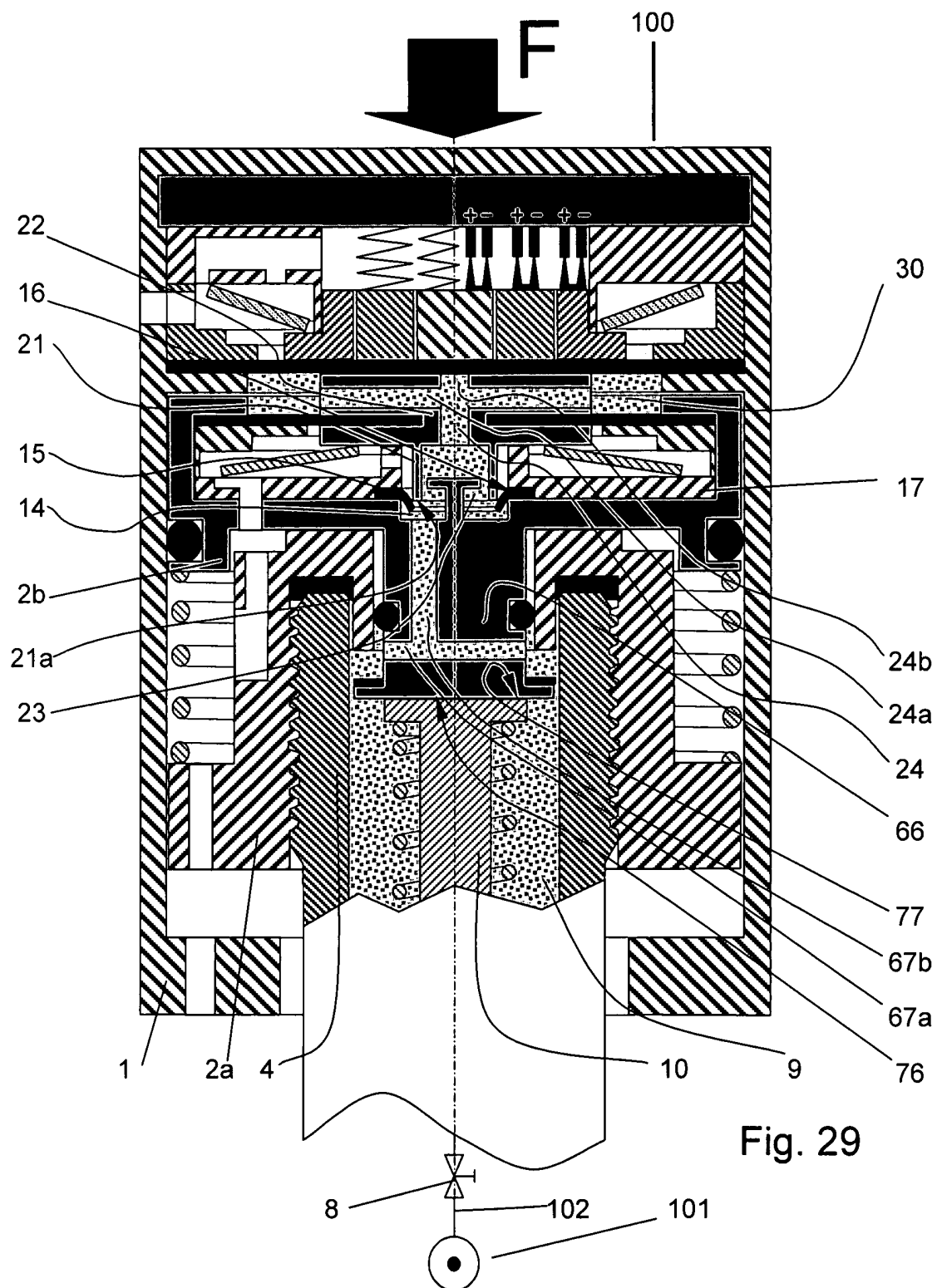


Fig. 29

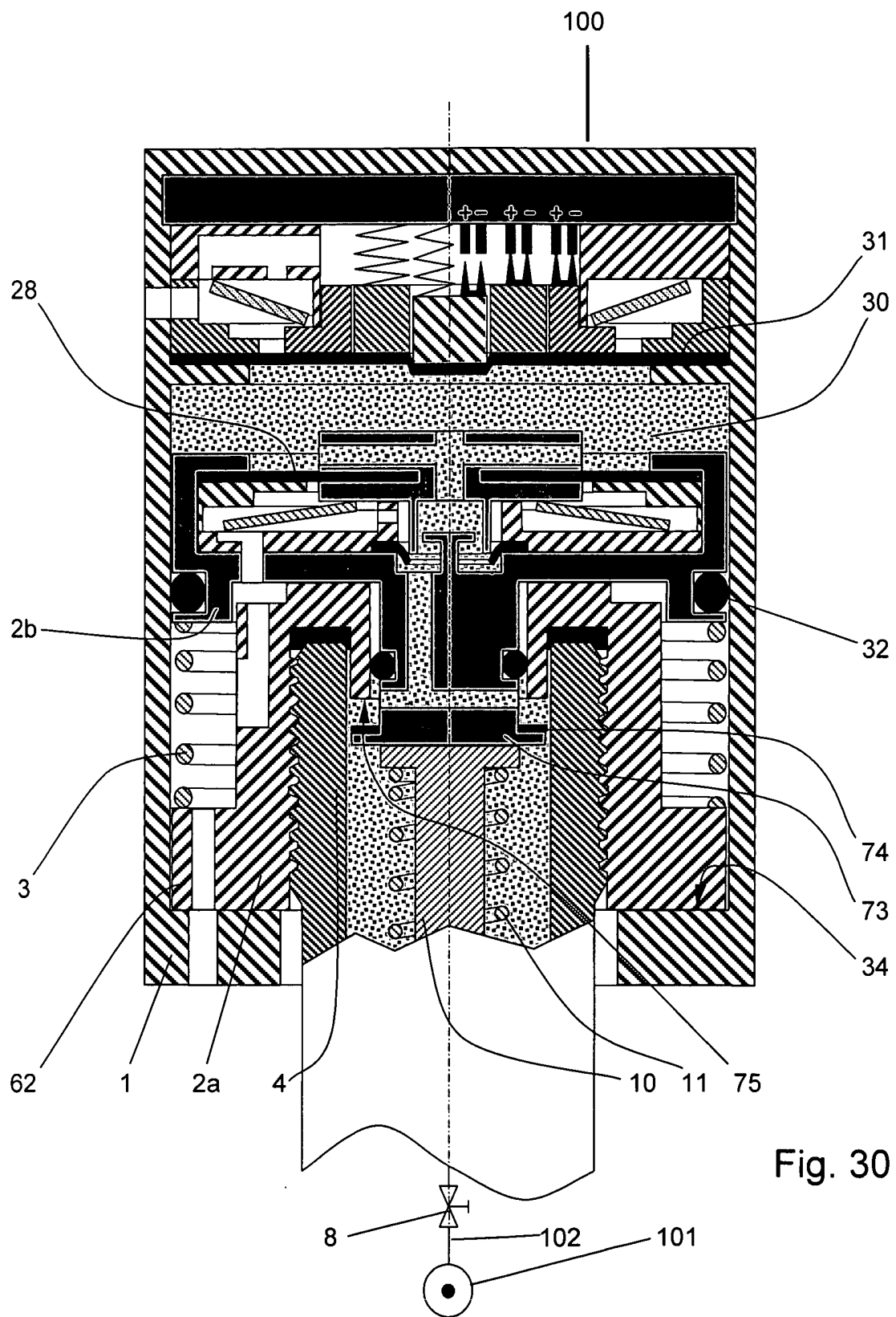


Fig. 30

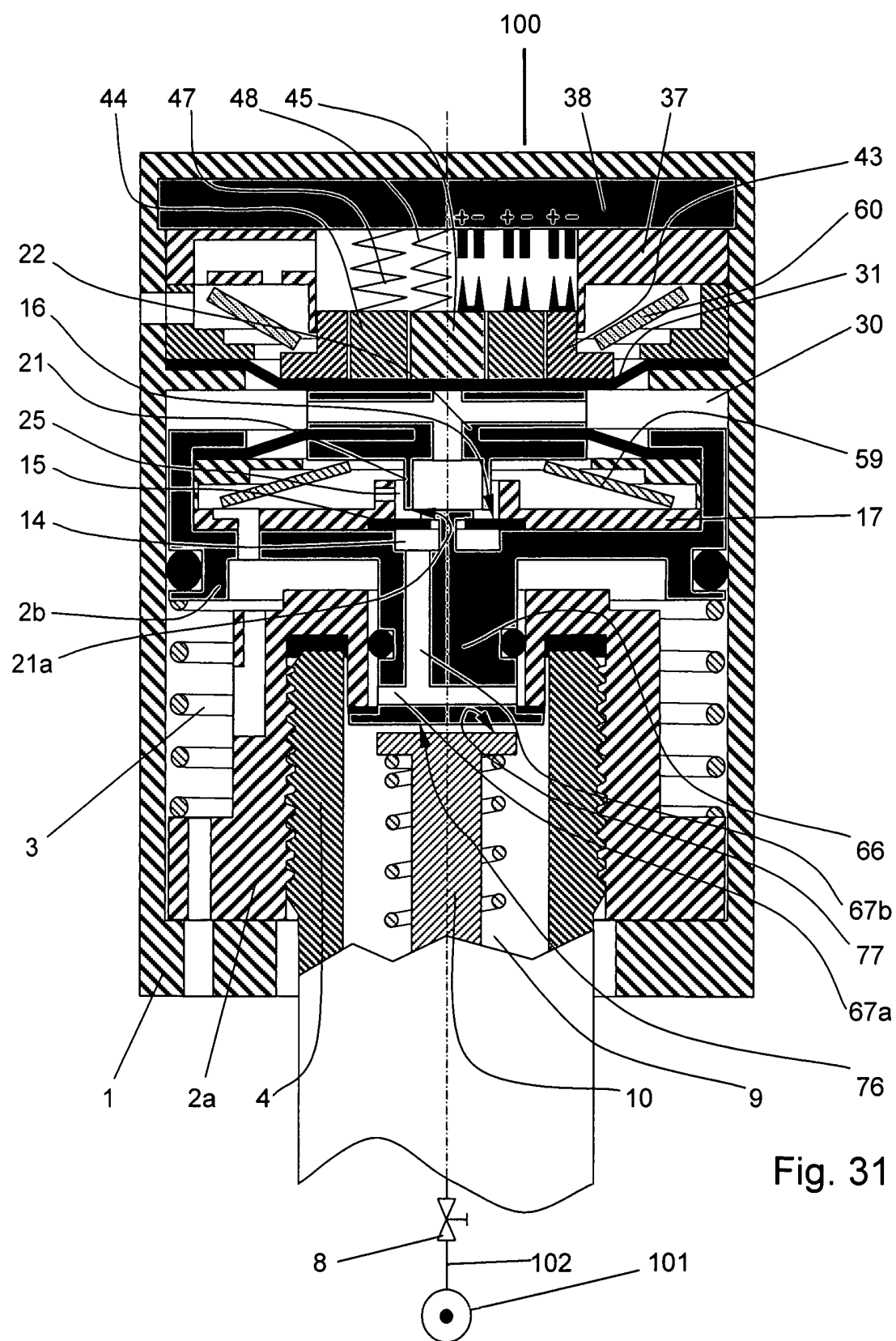


Fig. 31

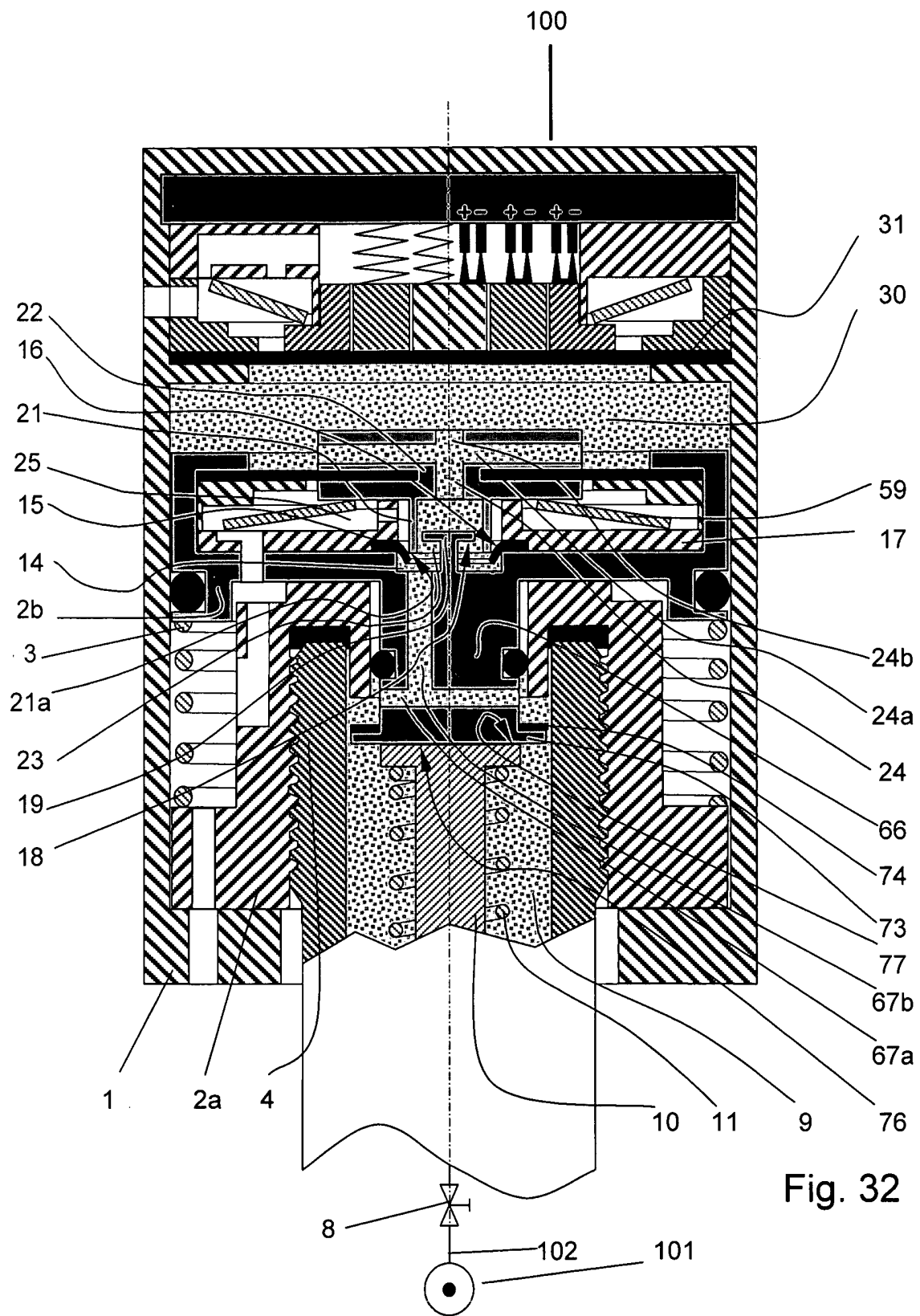
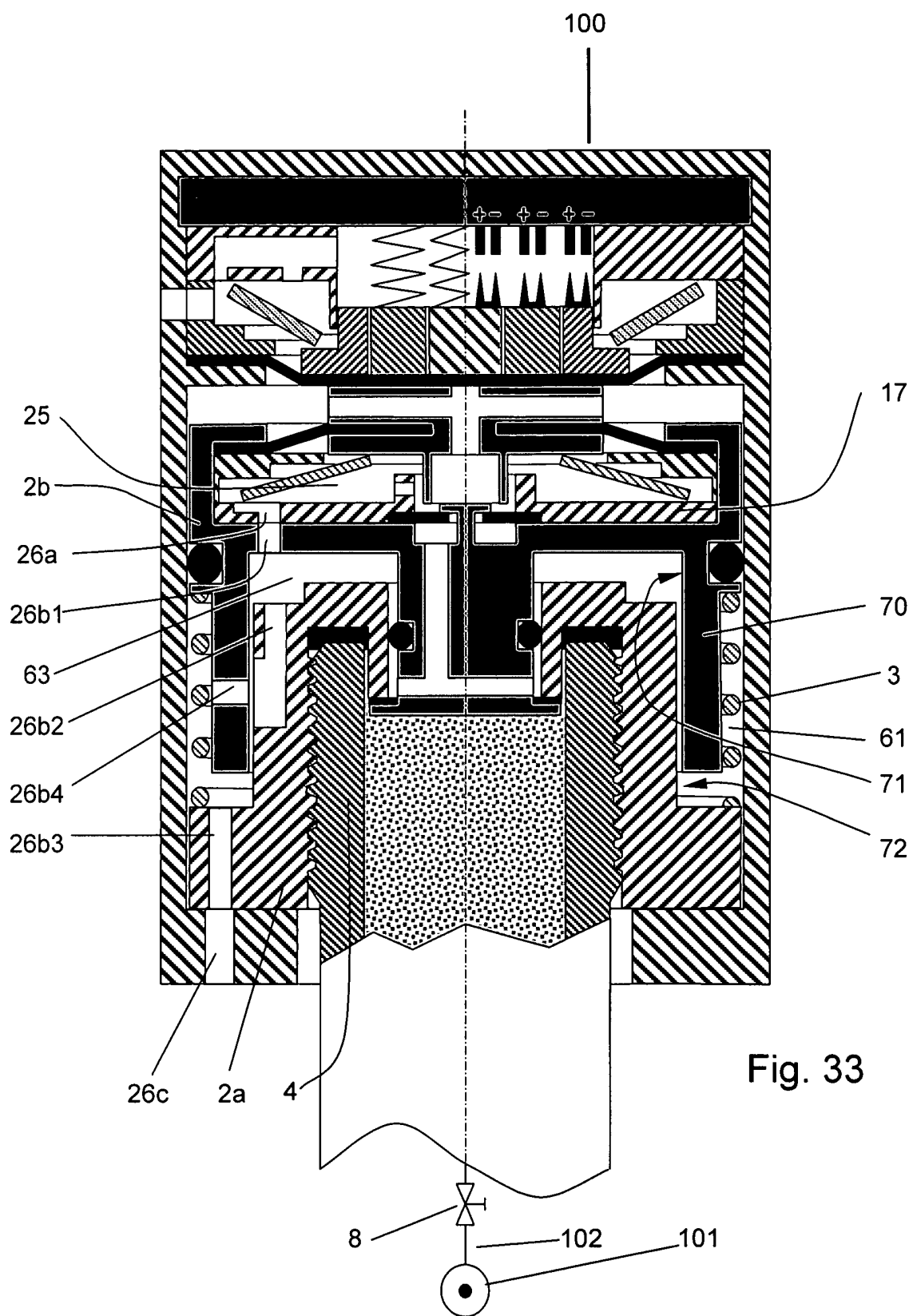


Fig. 32





[illegible]

Fig. 34b

This cross-sectional view shows a central channel region (100) flanked by side gates (51, 54, 48, 50, 53). The channel is formed in a substrate (30) and is covered by a gate stack (38). The side gates are formed in the substrate and are separated by insulating regions (43, 47, 80, 49, 52). The device is surrounded by a protective layer (37). The channel region is doped with p-type impurities (indicated by '+' signs) and the side gates are doped with n-type impurities (indicated by '-' signs). The channel region is also doped with p-type impurities (indicated by '+' signs). The side gates are doped with n-type impurities (indicated by '-' signs). The channel region is also doped with p-type impurities (indicated by '+' signs). The side gates are doped with n-type impurities (indicated by '-' signs).

Fig. 35a

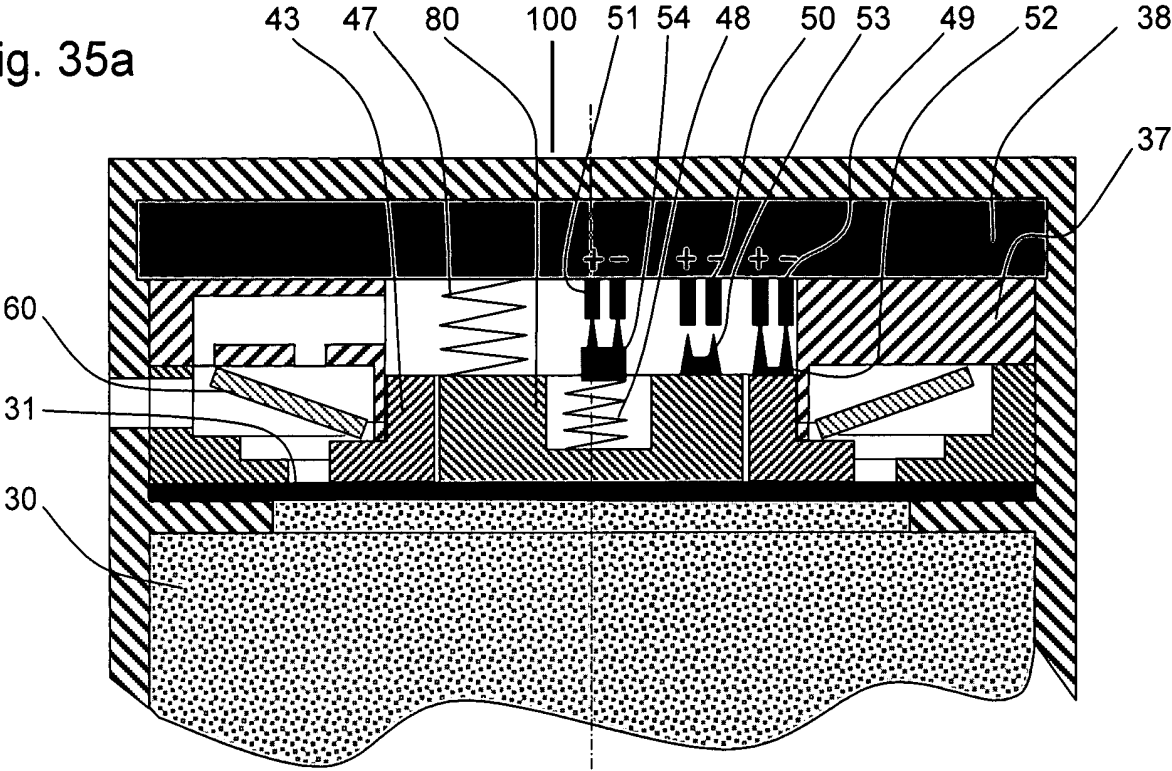


Fig. 35b

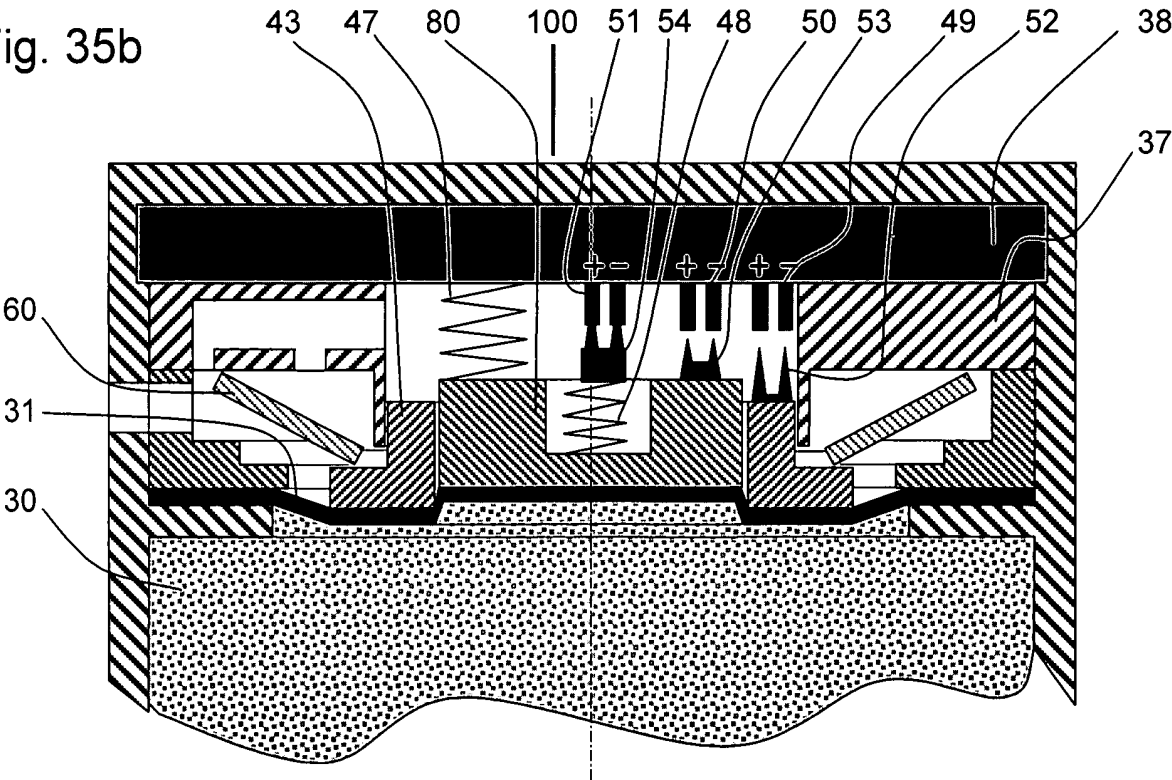


Fig. 36a

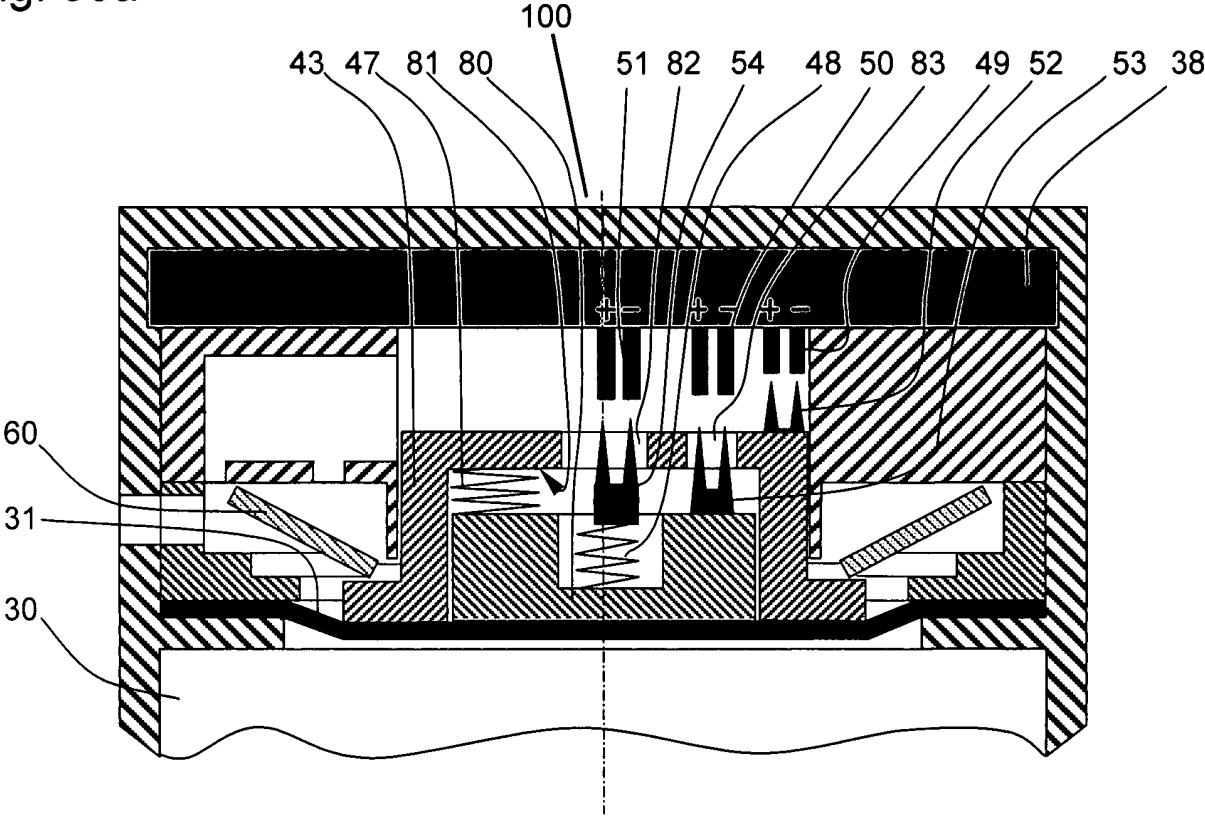


Fig. 36b

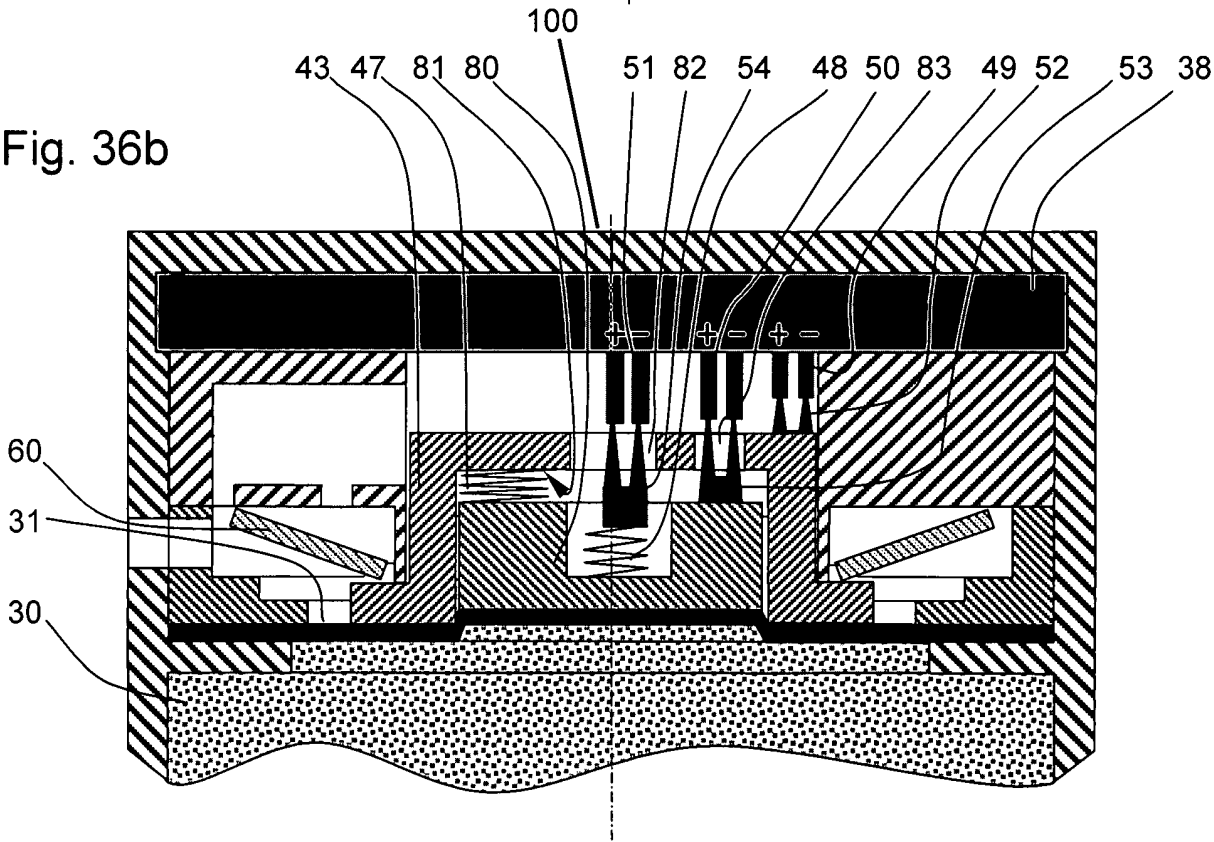


Fig. 37a

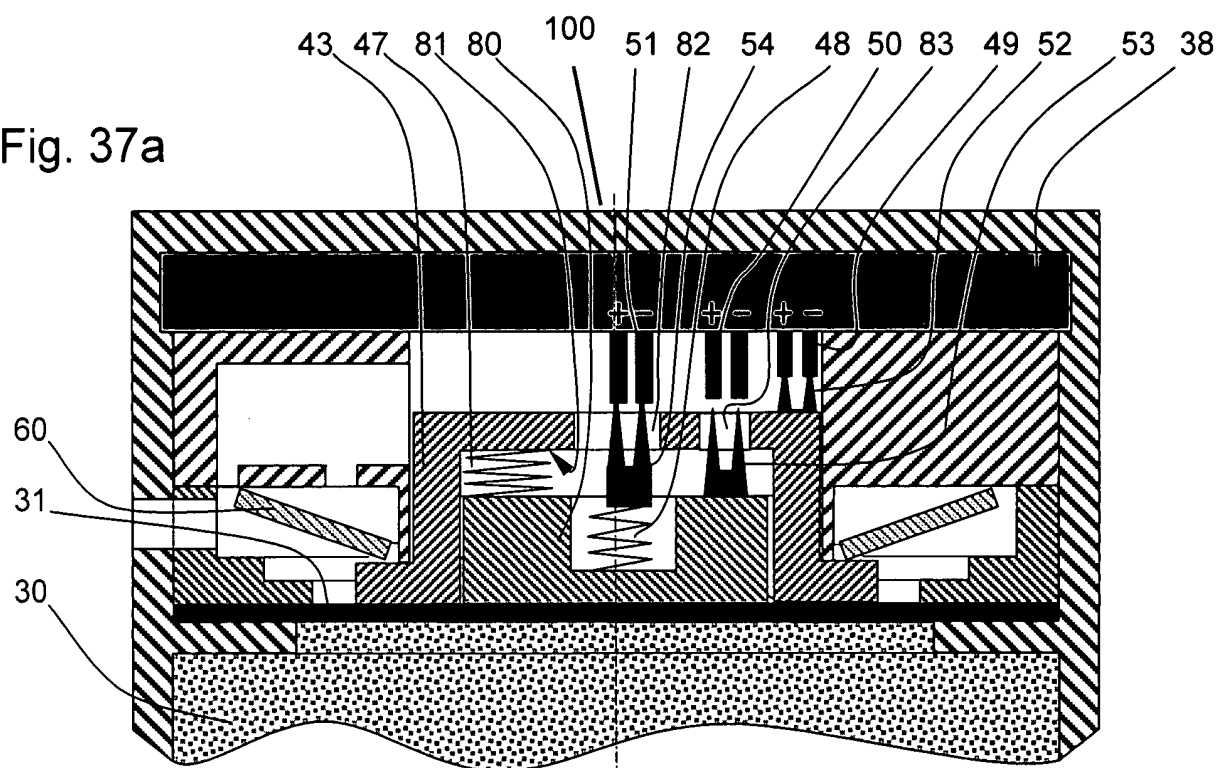
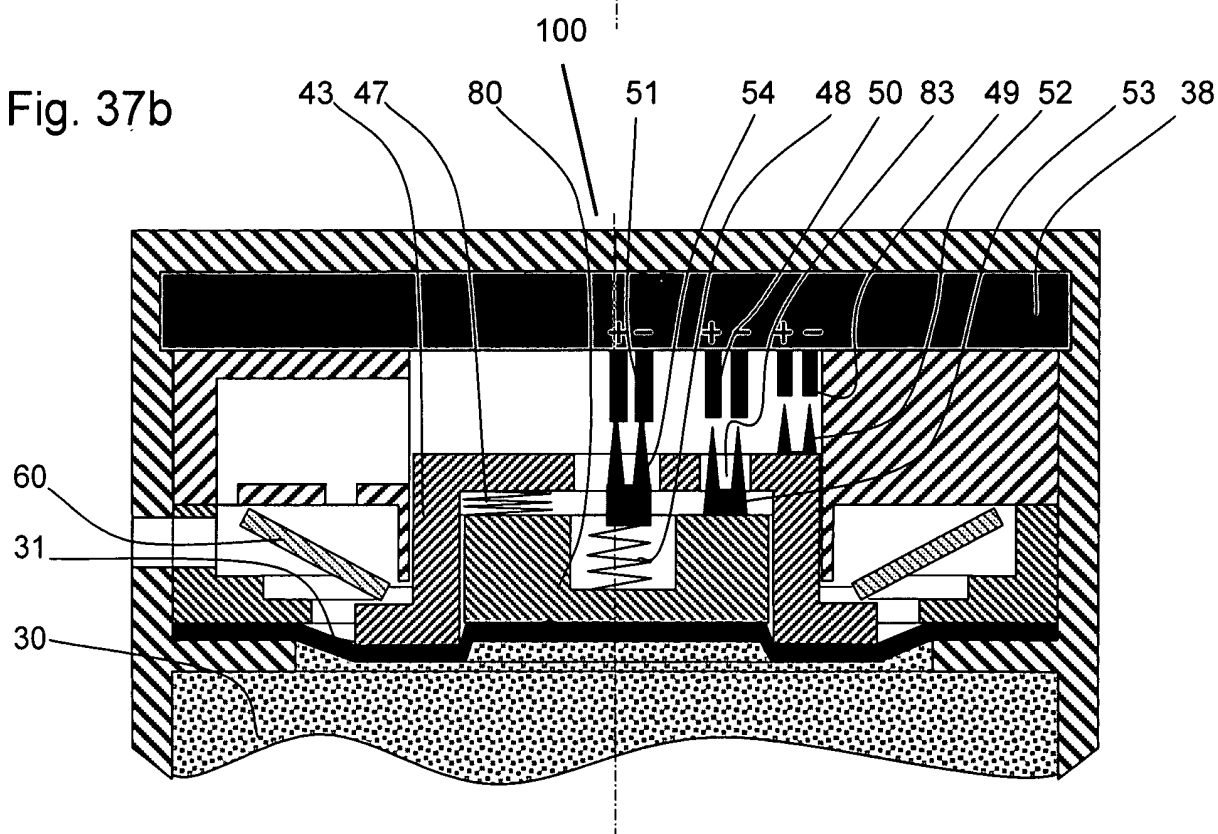


Fig. 37b



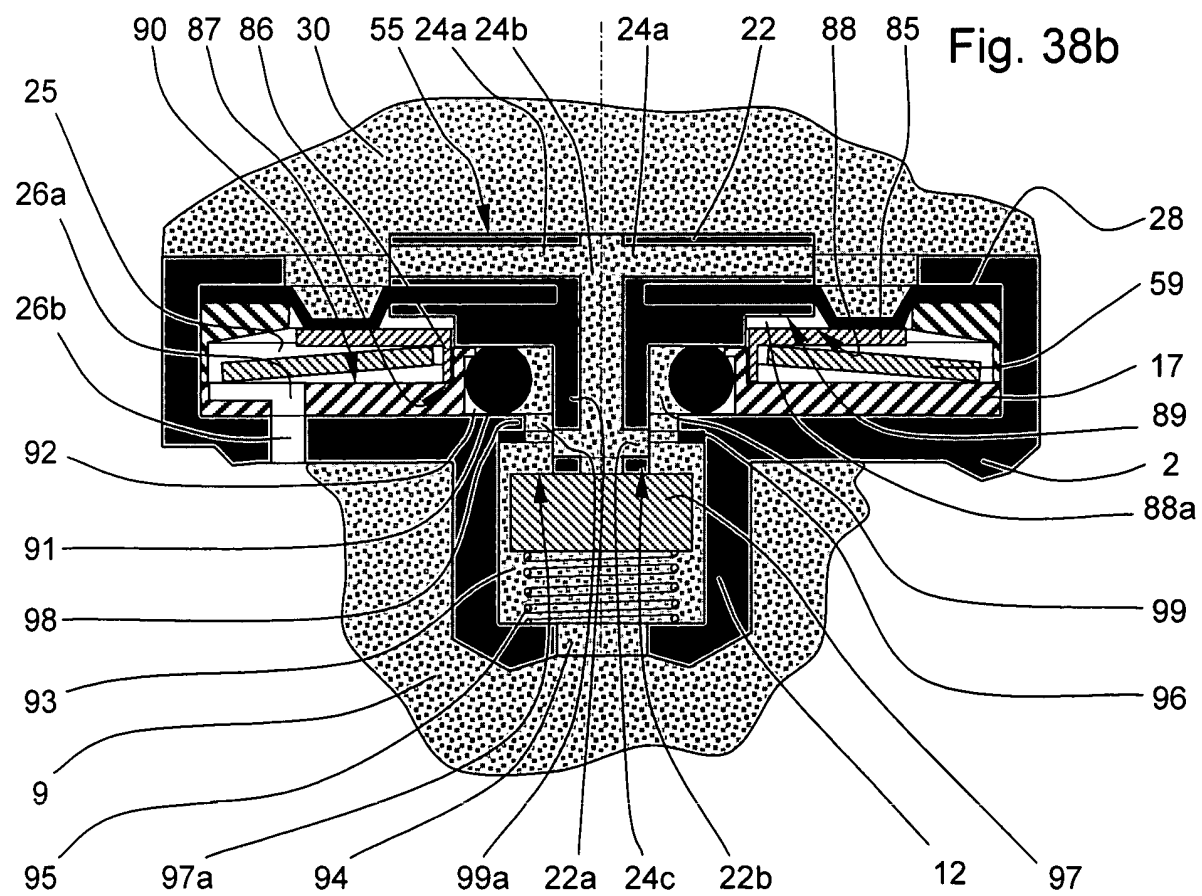
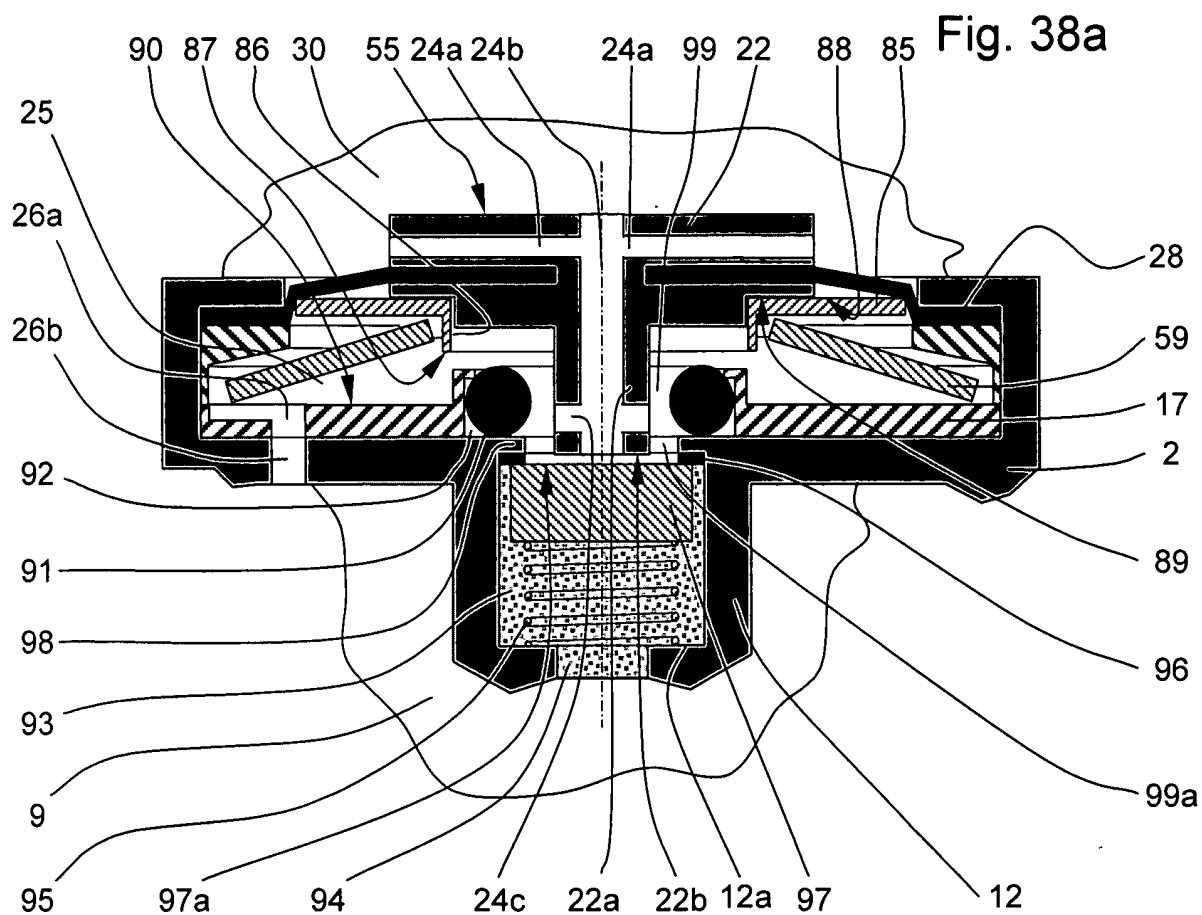


Fig. 39

